

1 of 51

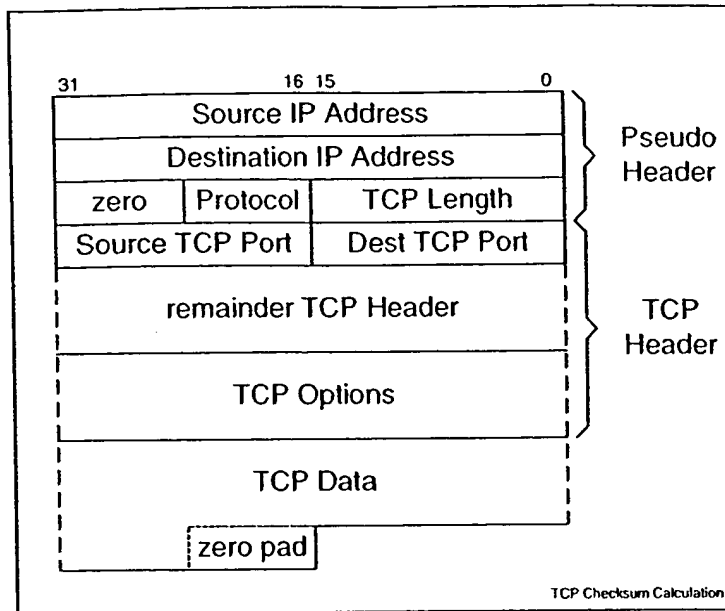


Figure 1A (PRIOR ART)

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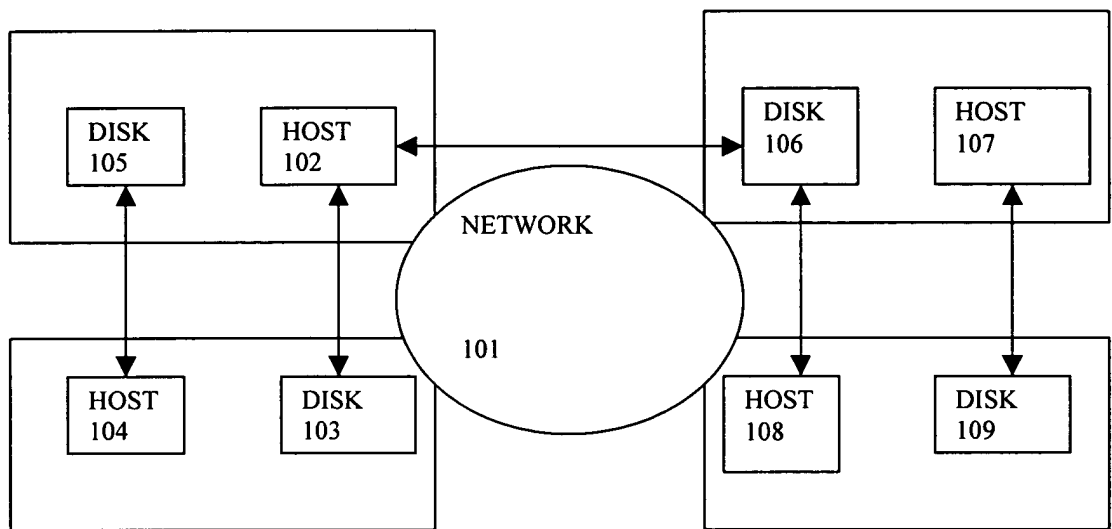


FIGURE 2A

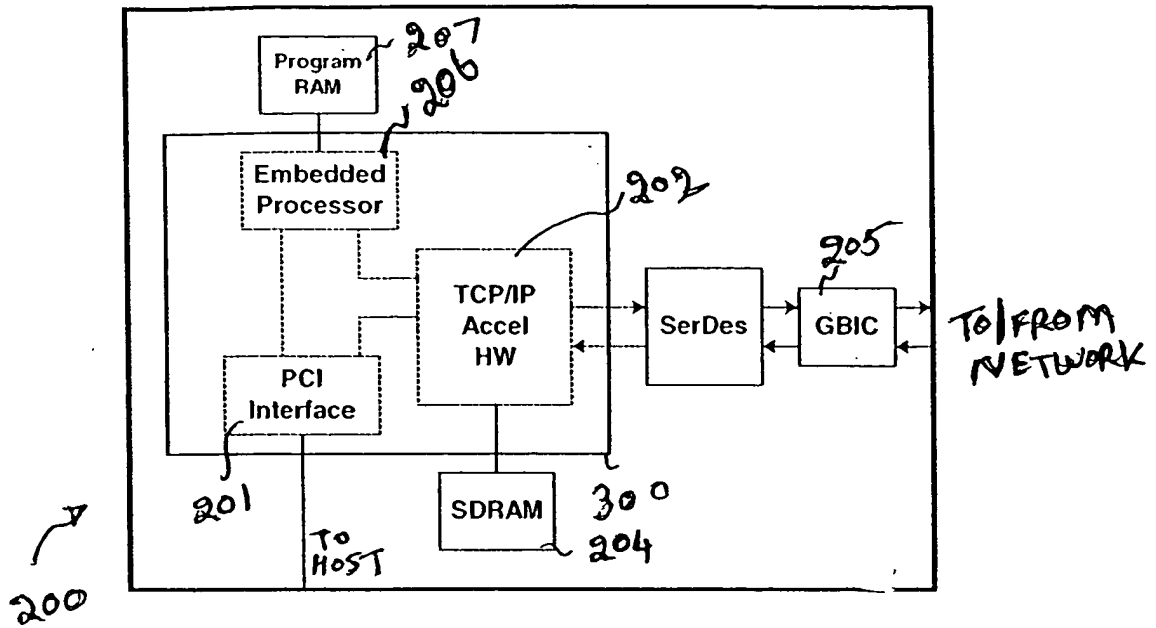


Figure 2B

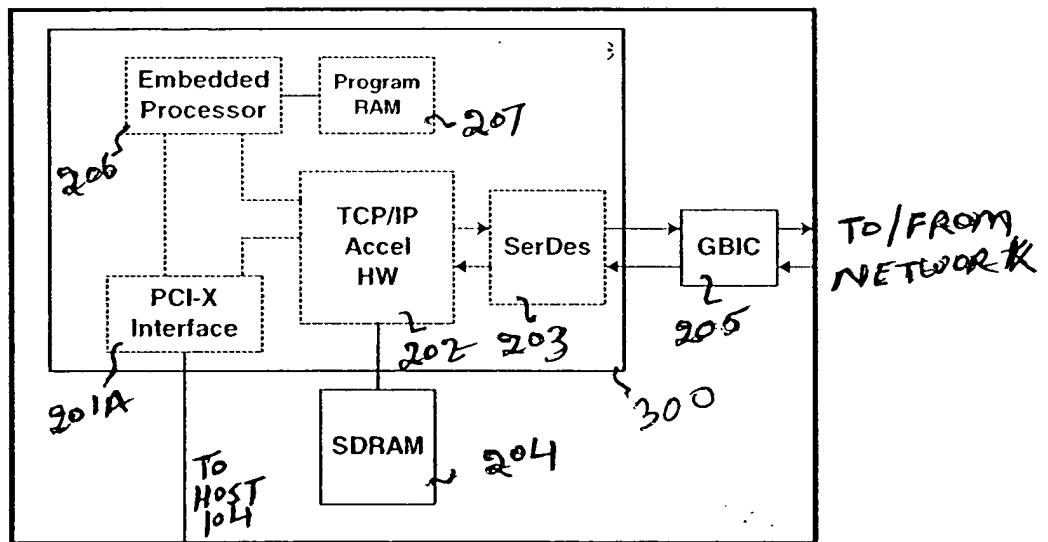


Figure 2C

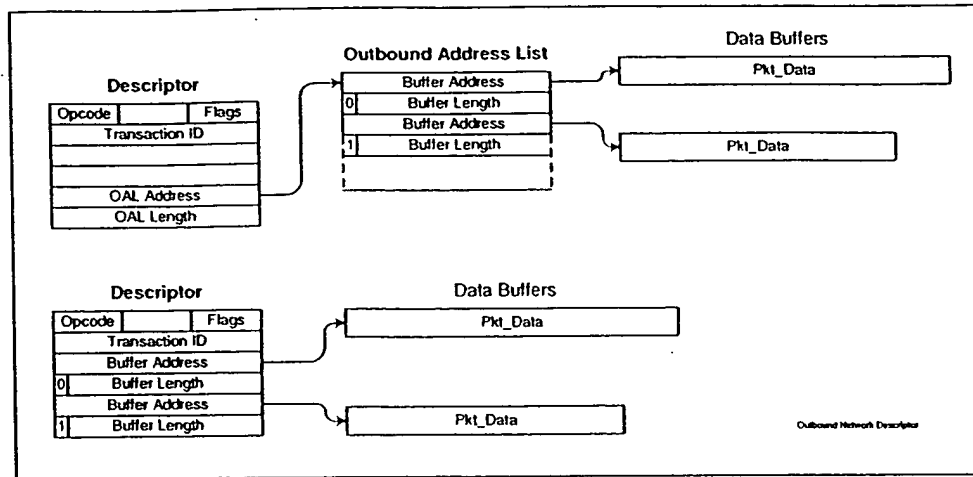


Figure 3A-1 Network IOCB Example

To Host 104

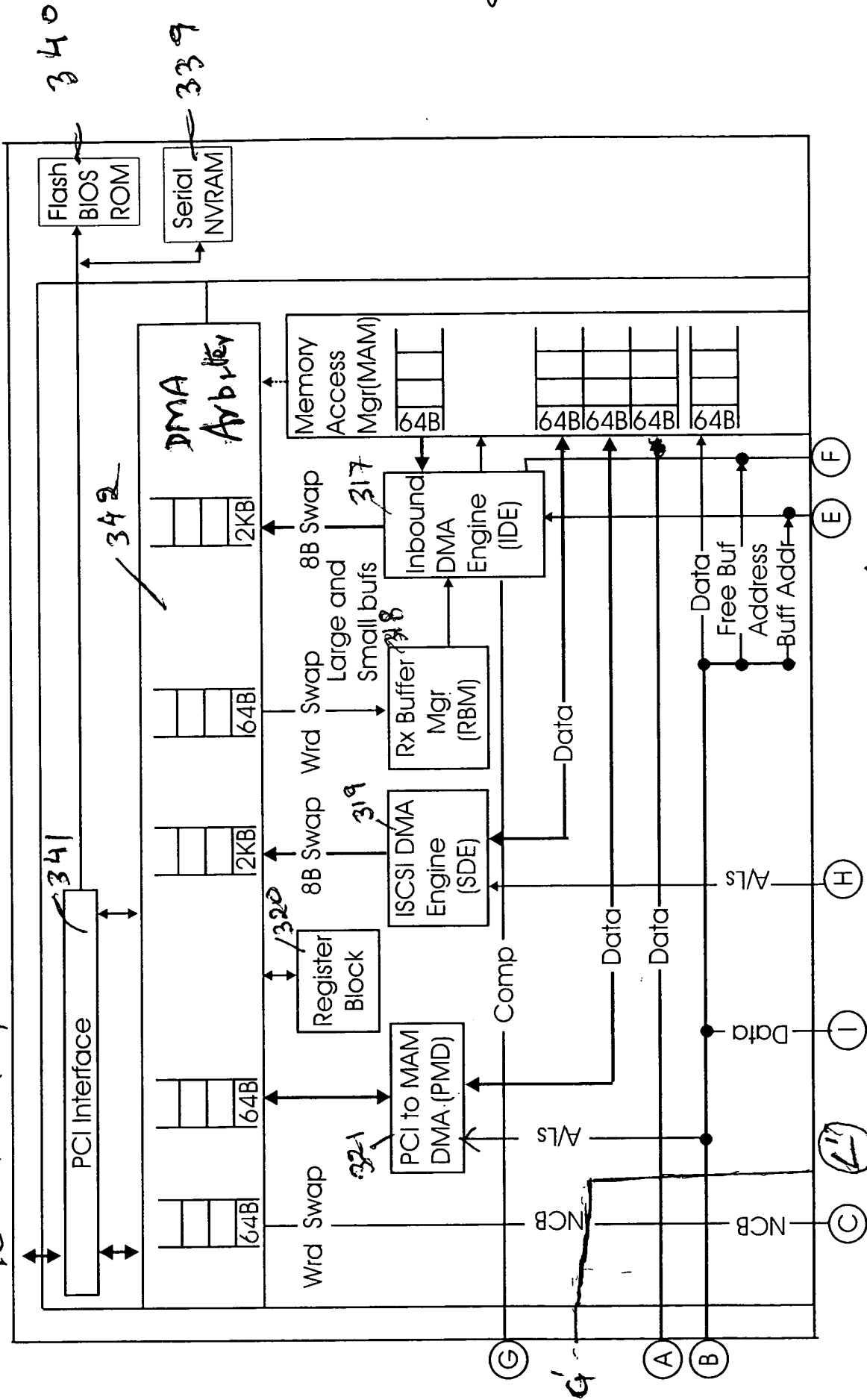


FIGURE 3A1-1

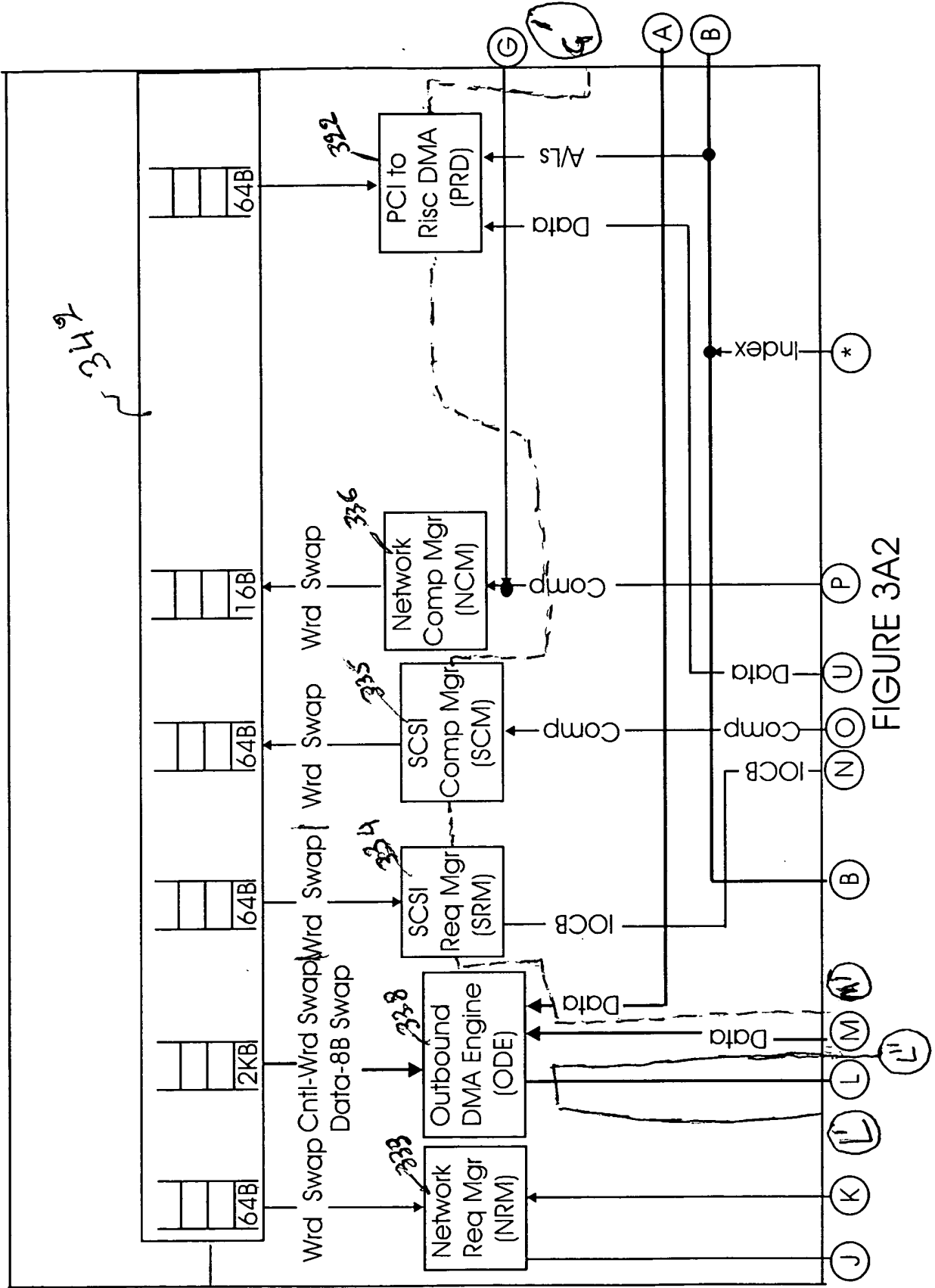


FIGURE 3A2

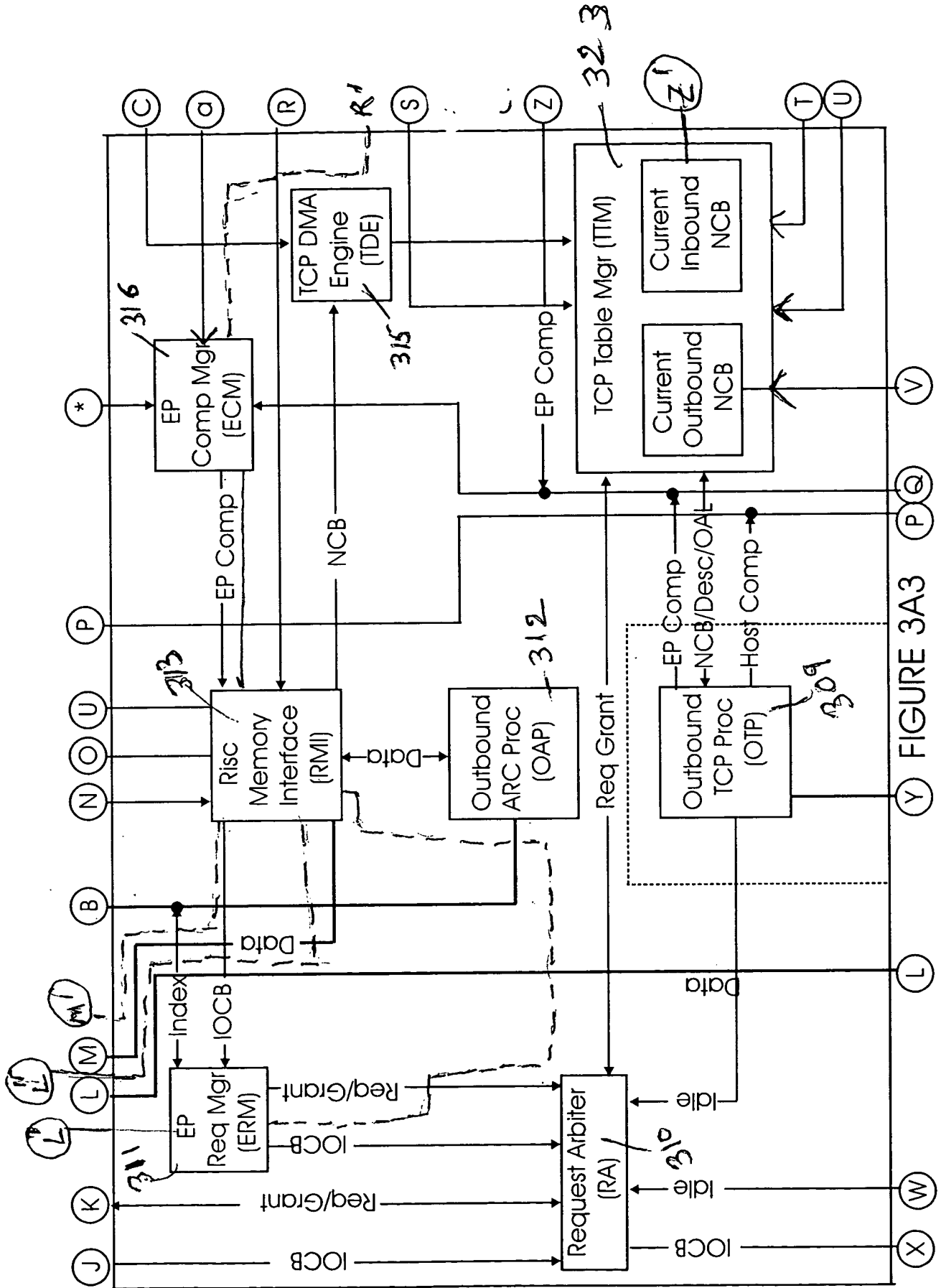


FIGURE 3A3

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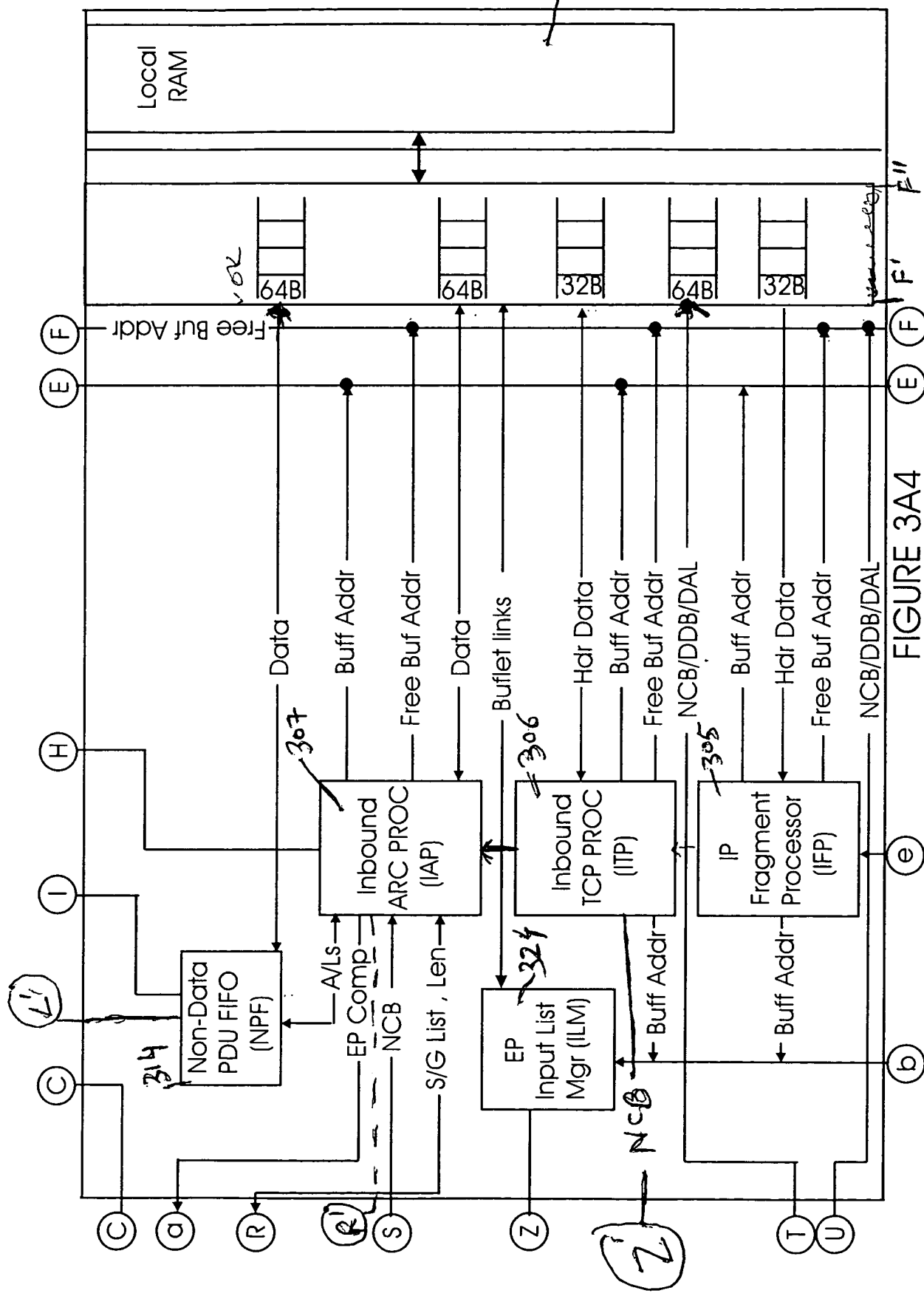
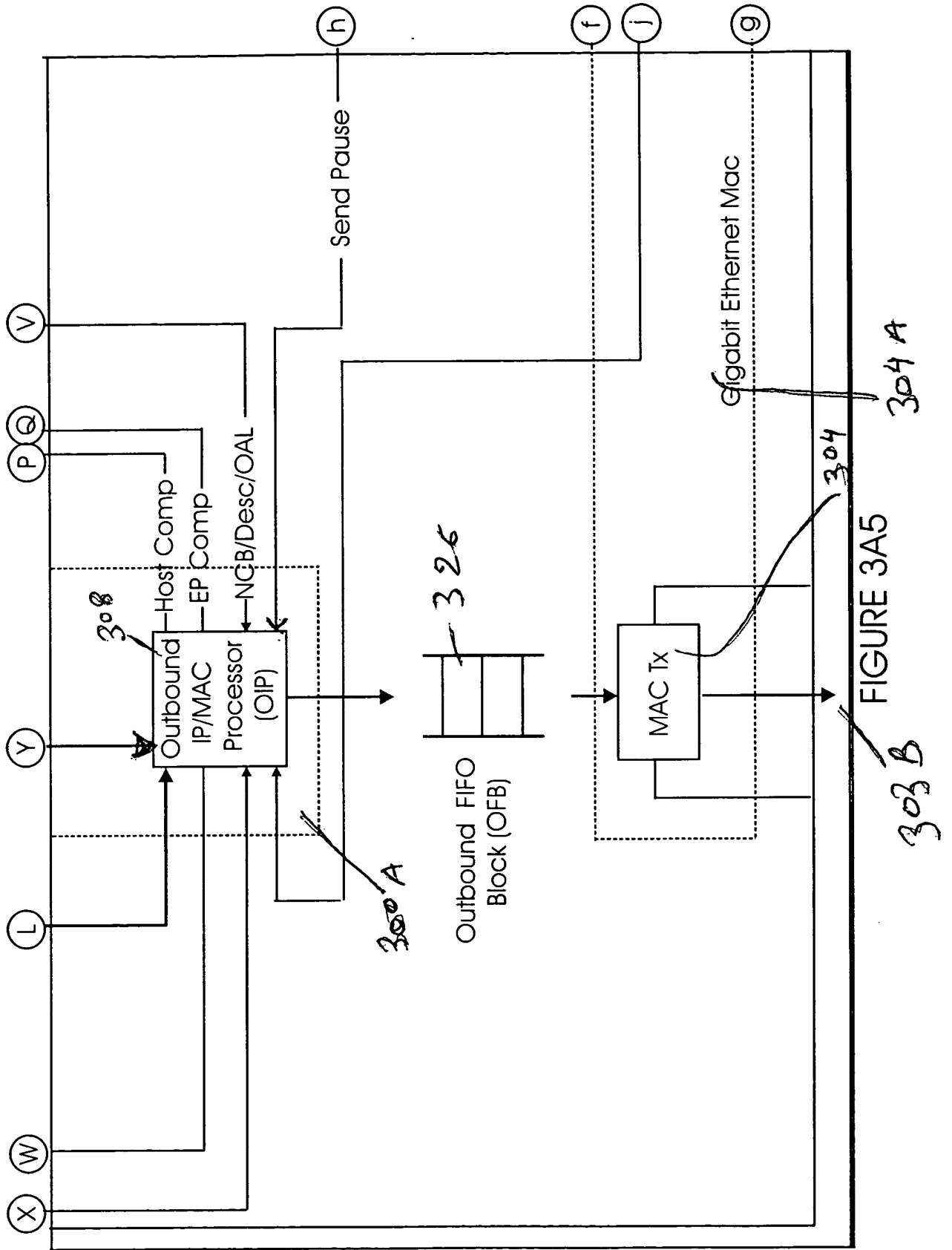


FIGURE 3A4



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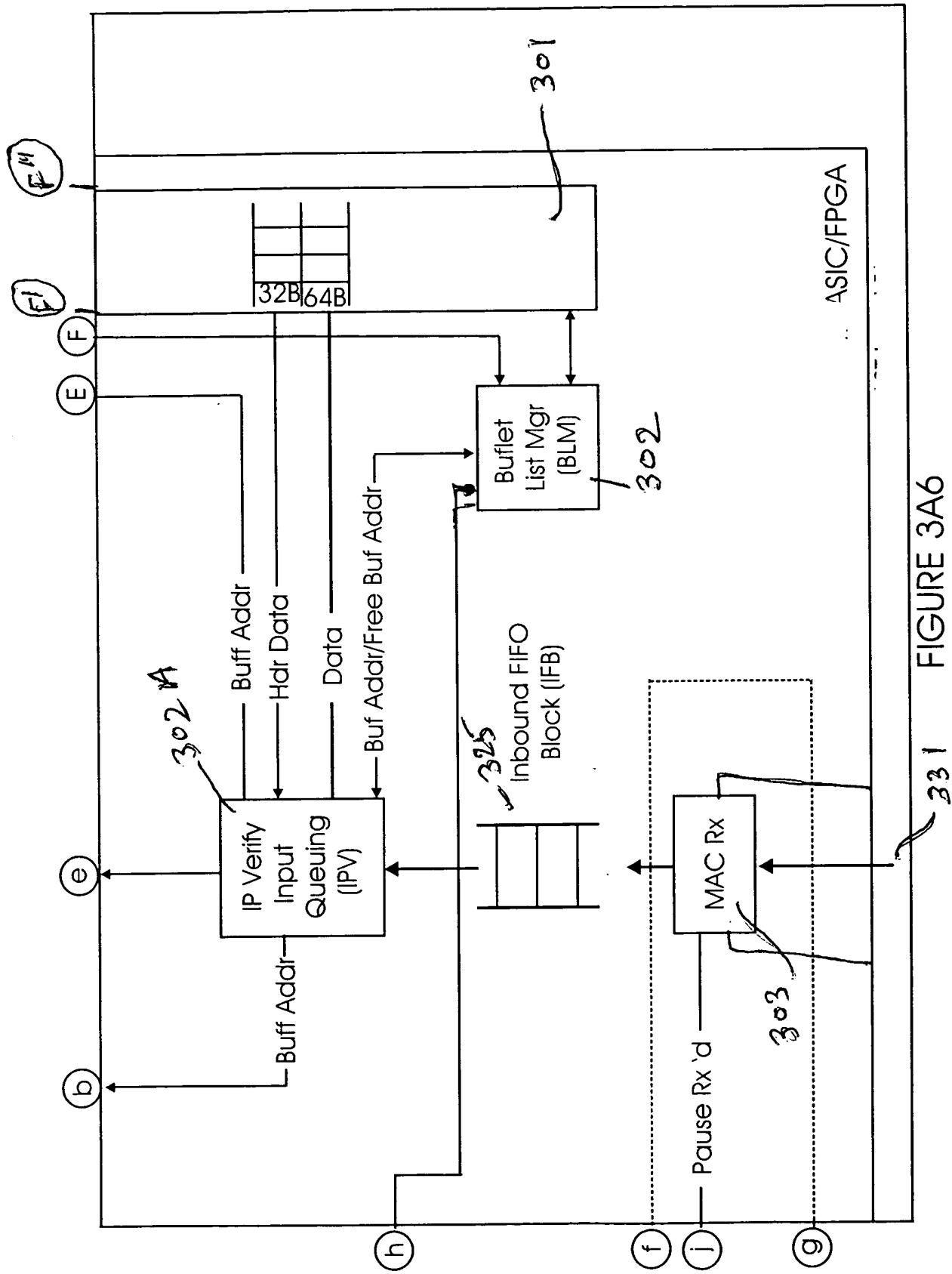


FIGURE 3A6

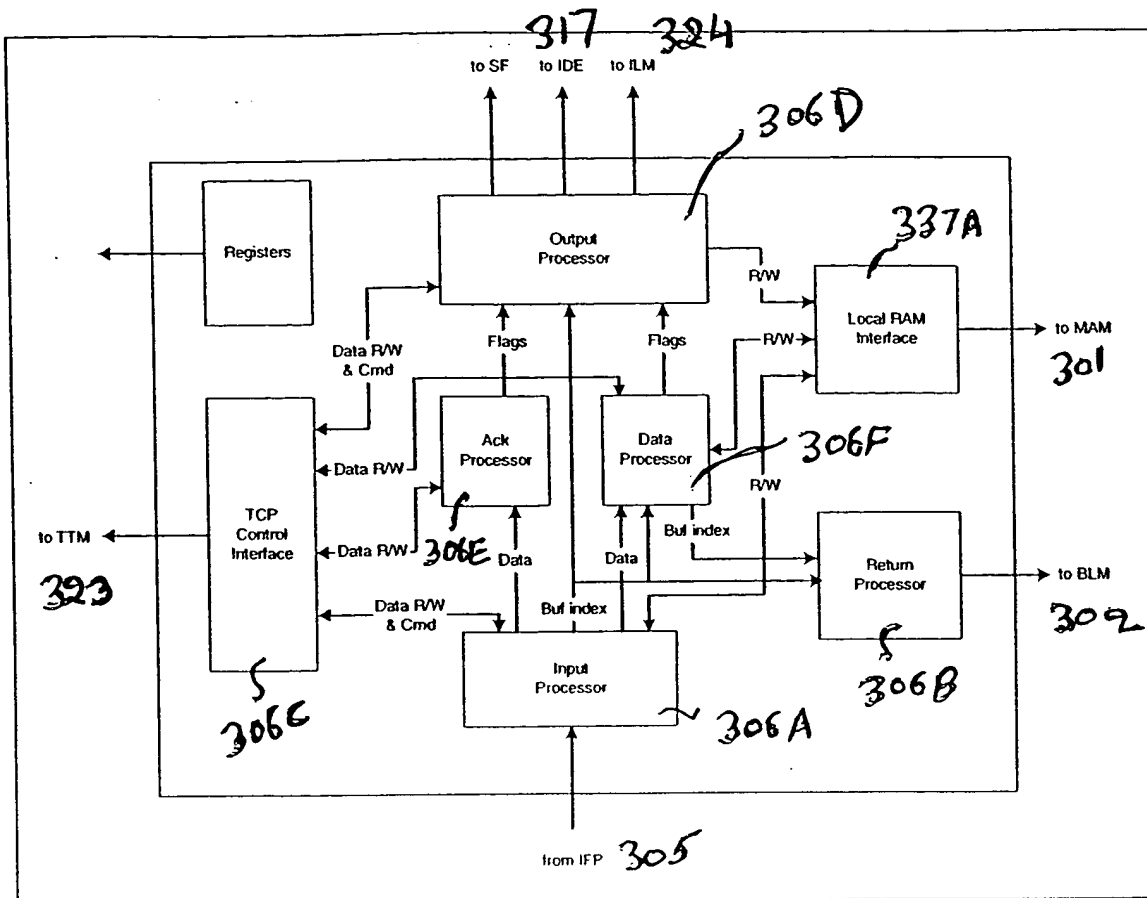


Figure 3B -

306

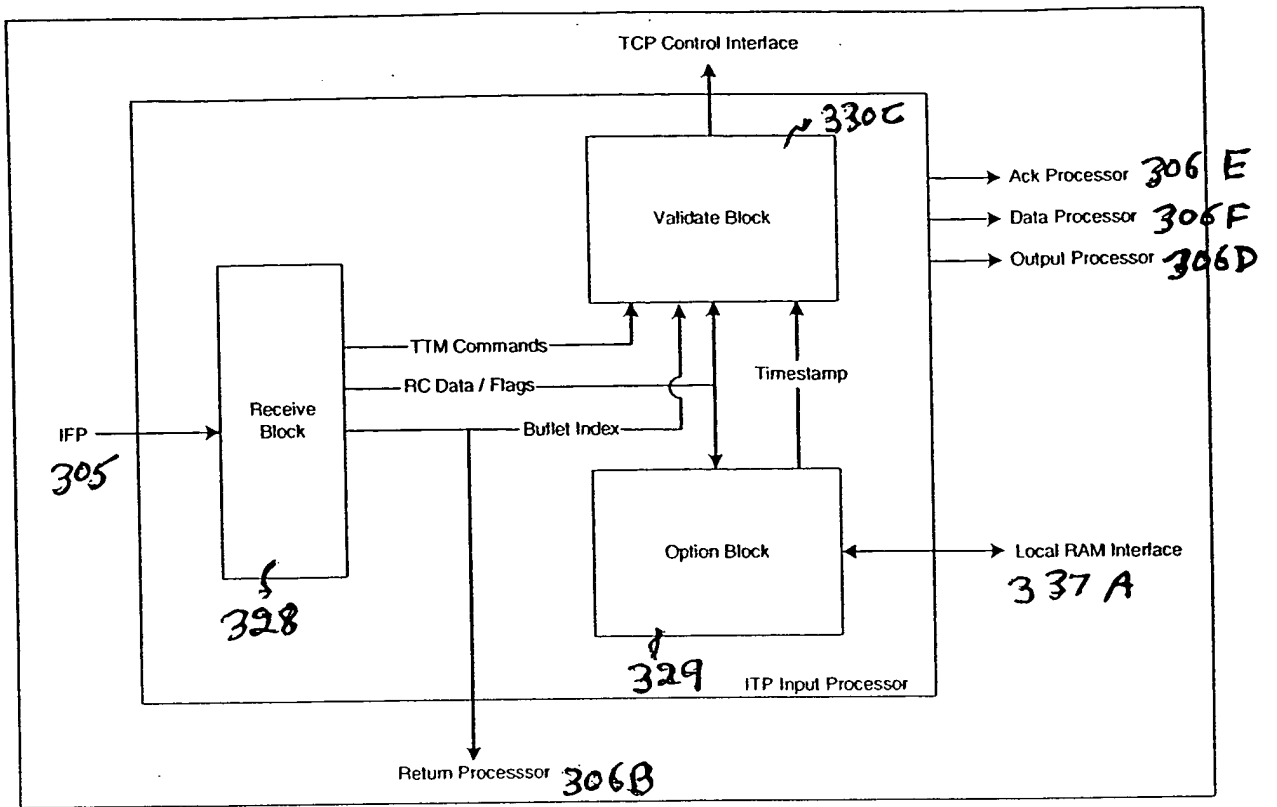


Figure 3C - ITP Input Processor

306A

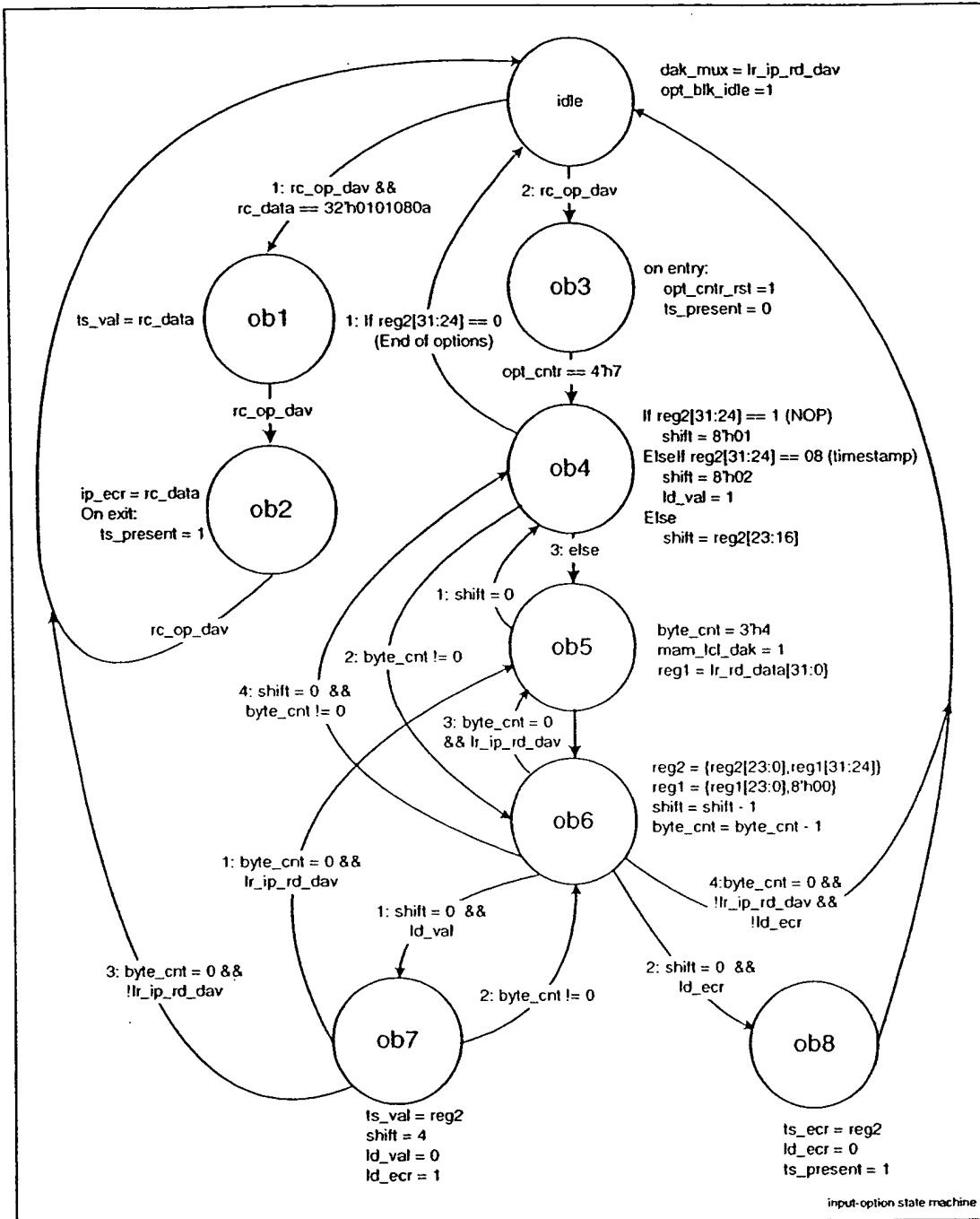


Figure 3C1 Input Processor Option Block State Machine

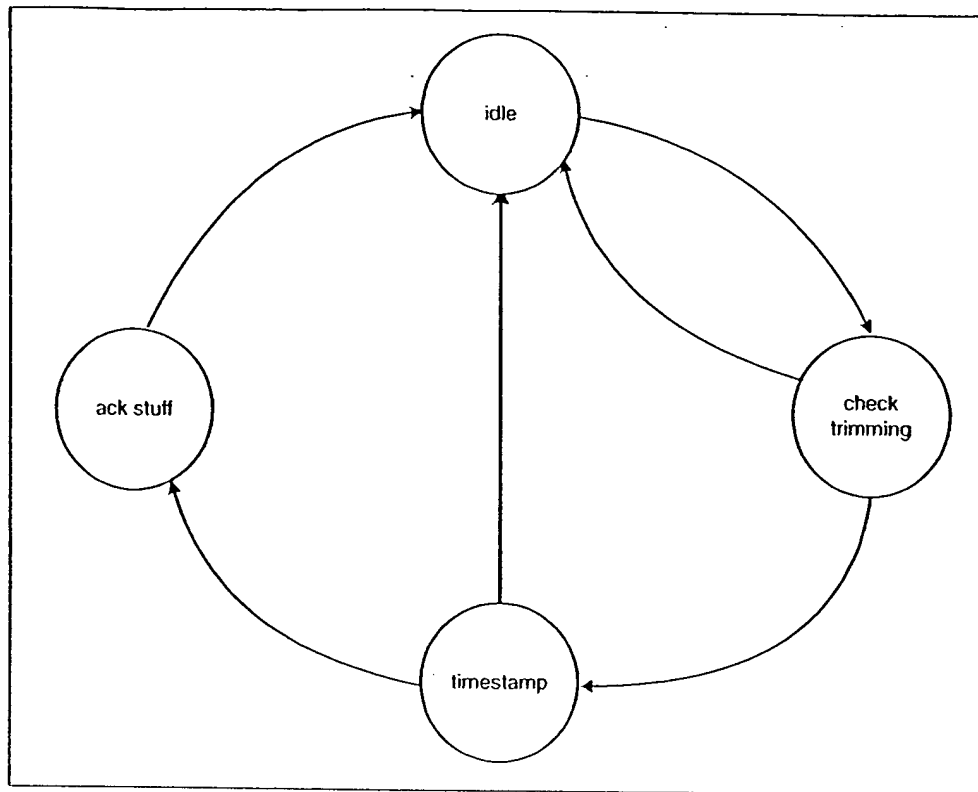


Figure 3C2: Validation State Machine

150651

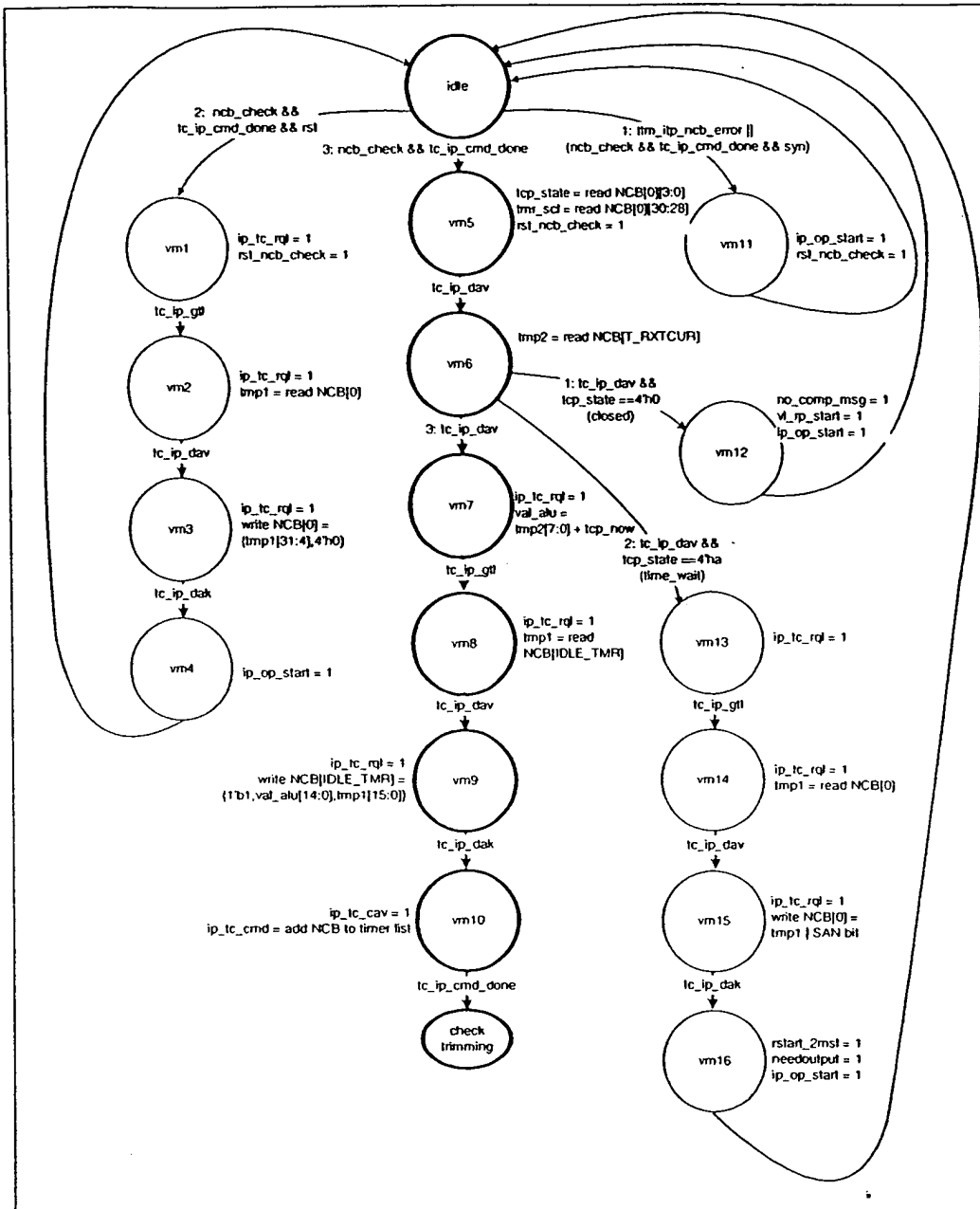


Figure 3C3: Validation State Machine for Reset, SYN or Invalid State

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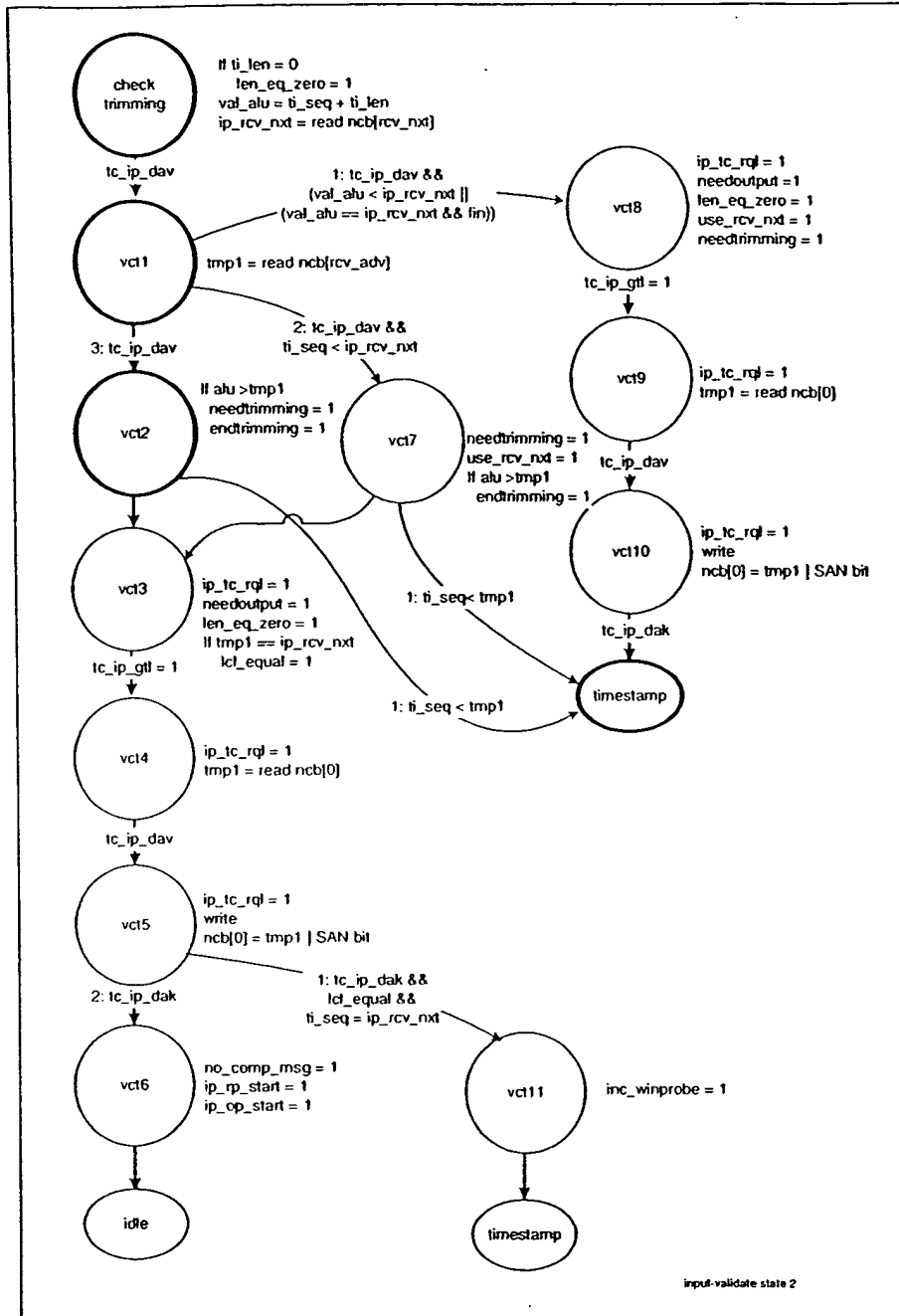


Figure 3C4: Validation State Machine for Trimming

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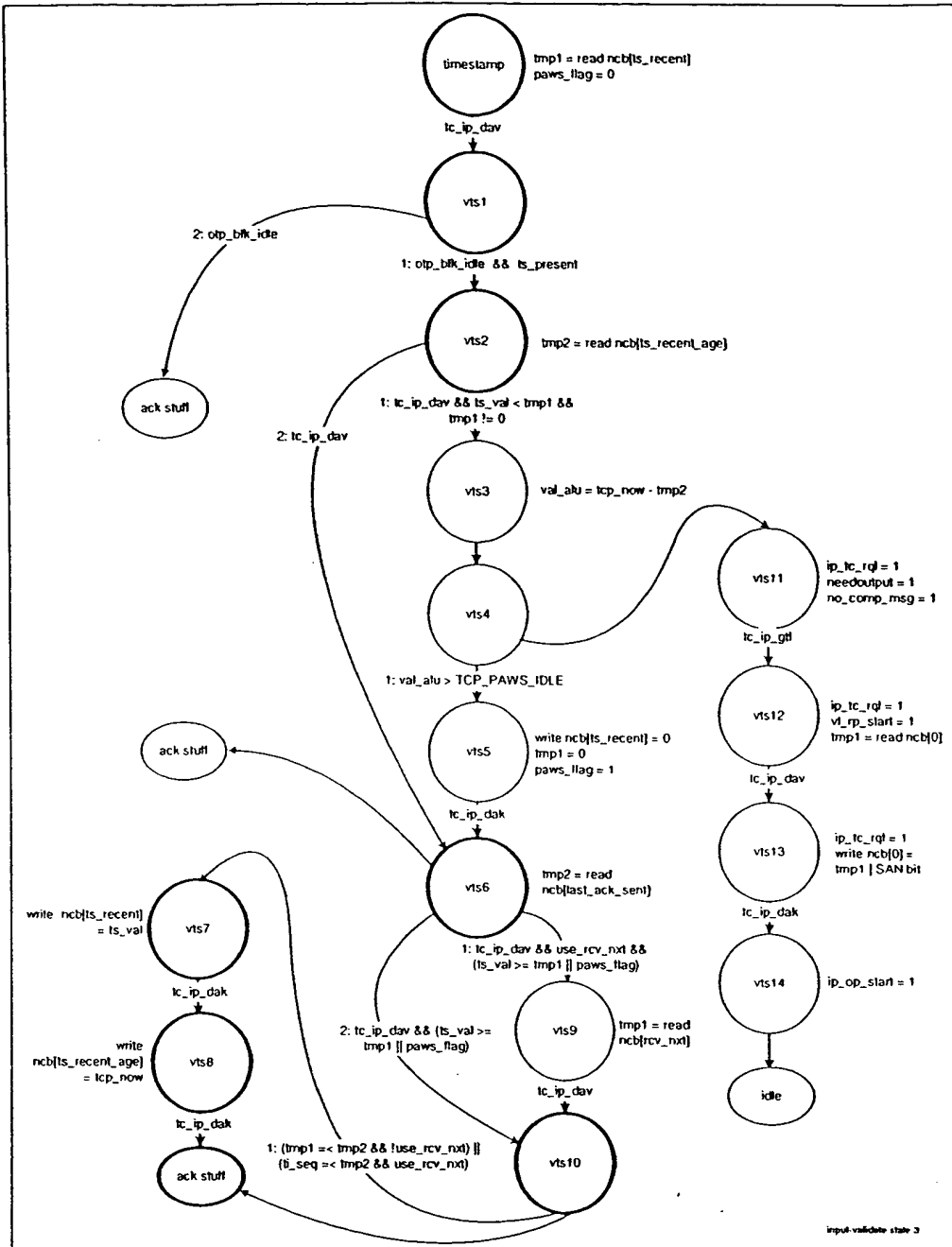


Figure 3C5: Validation State machine for Timestamp functionality

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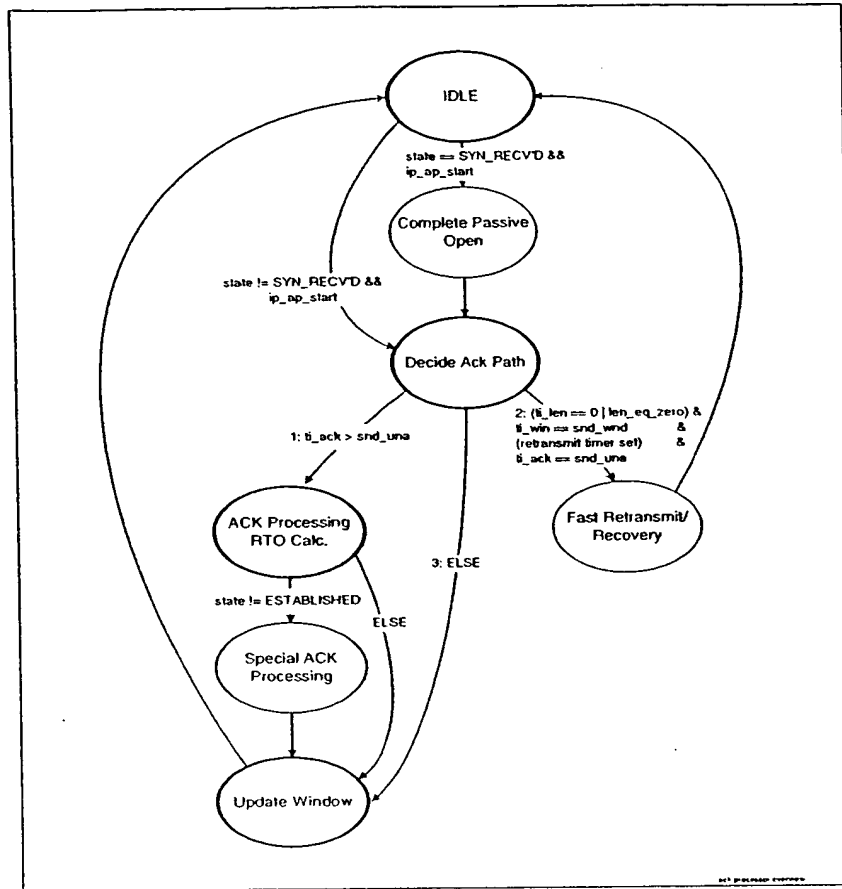


Figure3C6- ACK Processor

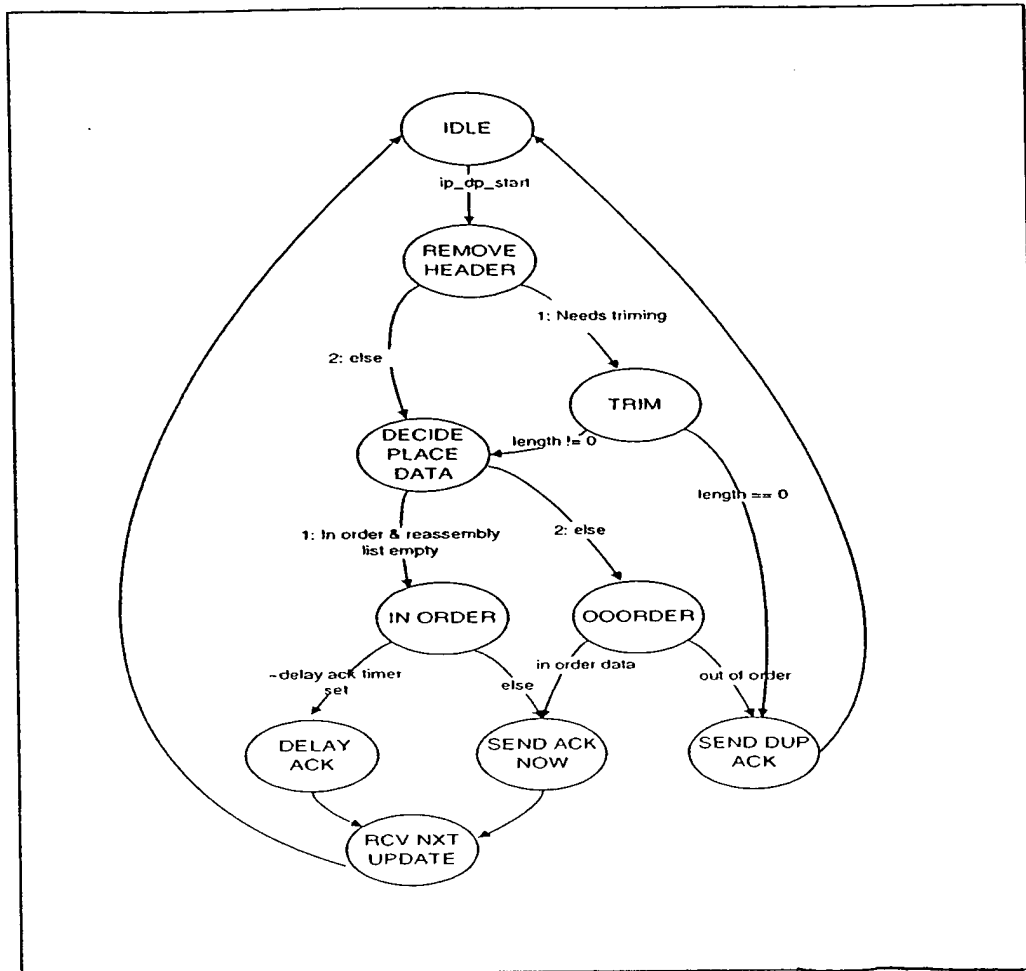


Figure 3C7 Data Processing State Machine

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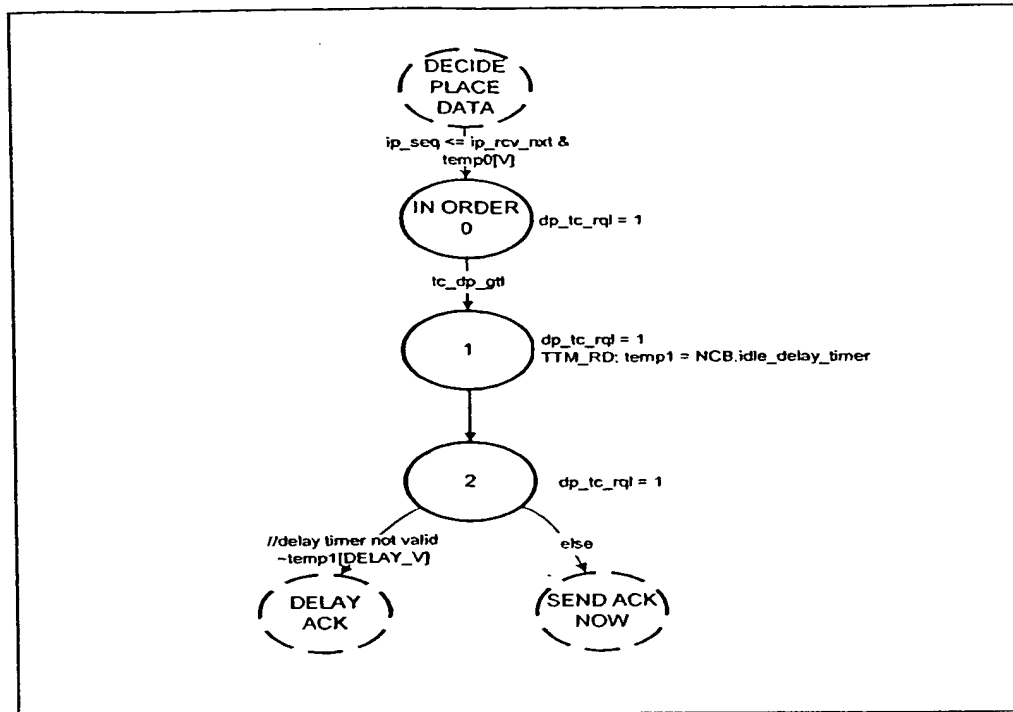


Figure 3C8 – Data Process Inorder

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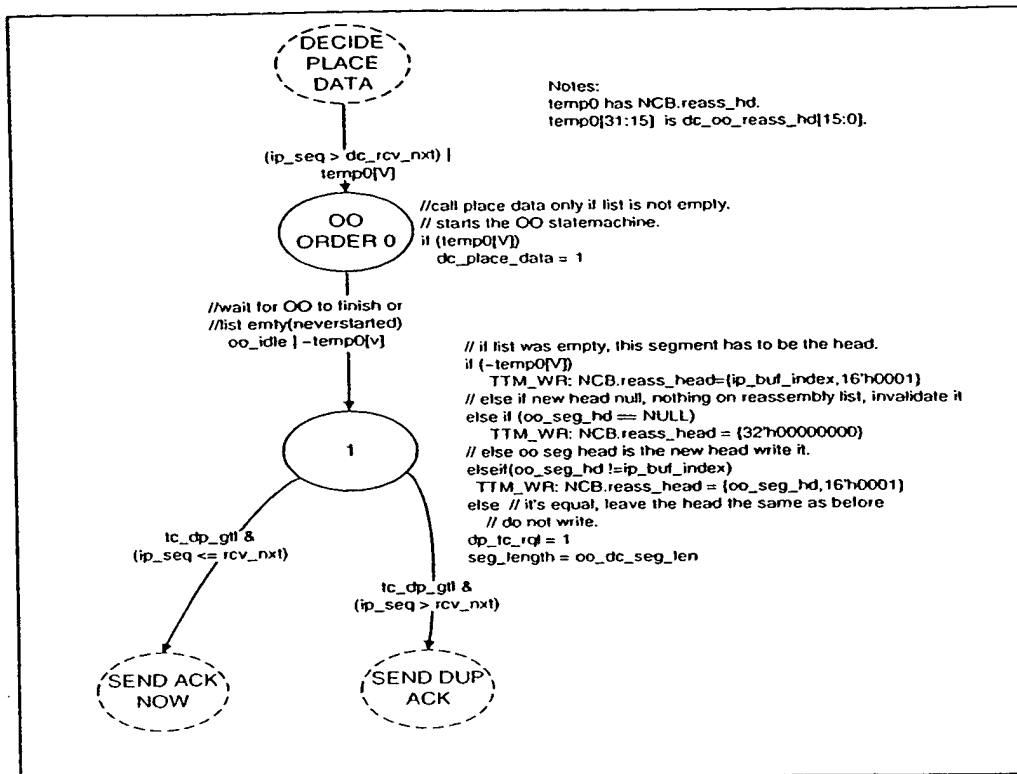


Figure 3C9 – Data Process Out of Order

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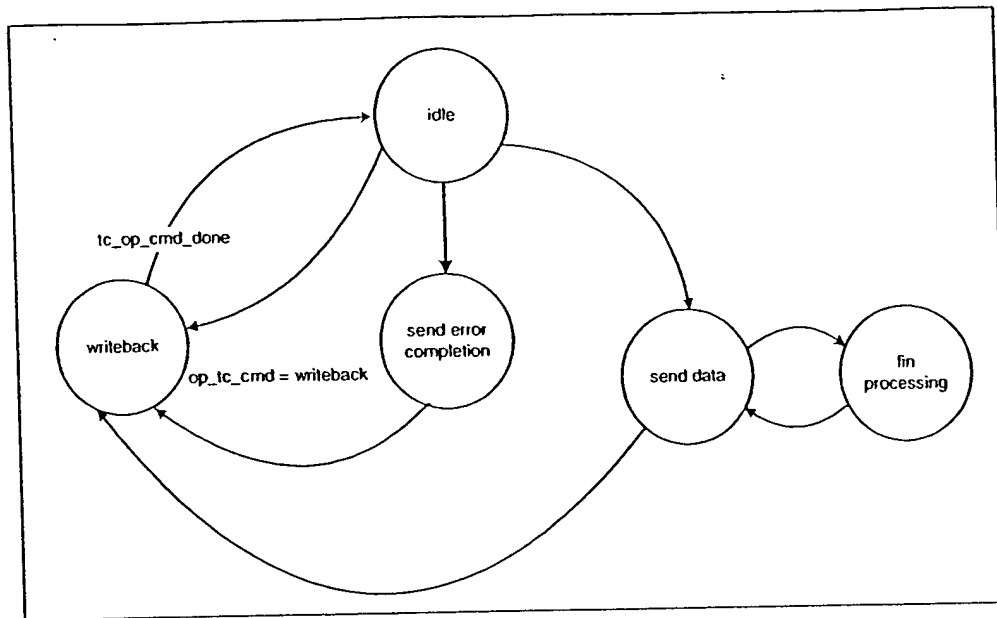


Figure 3C10 - ITP Output Processor (

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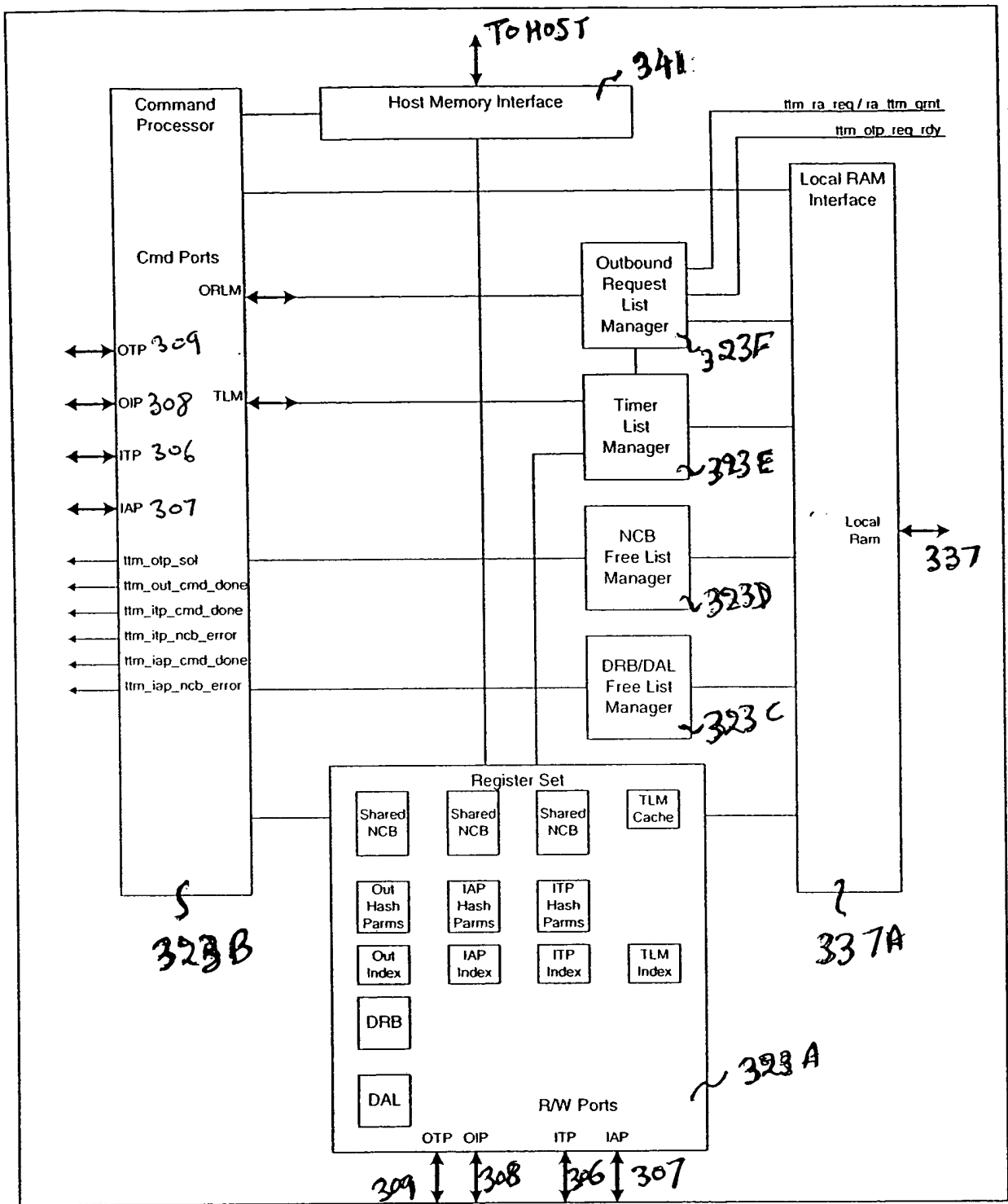


Figure 3D 7

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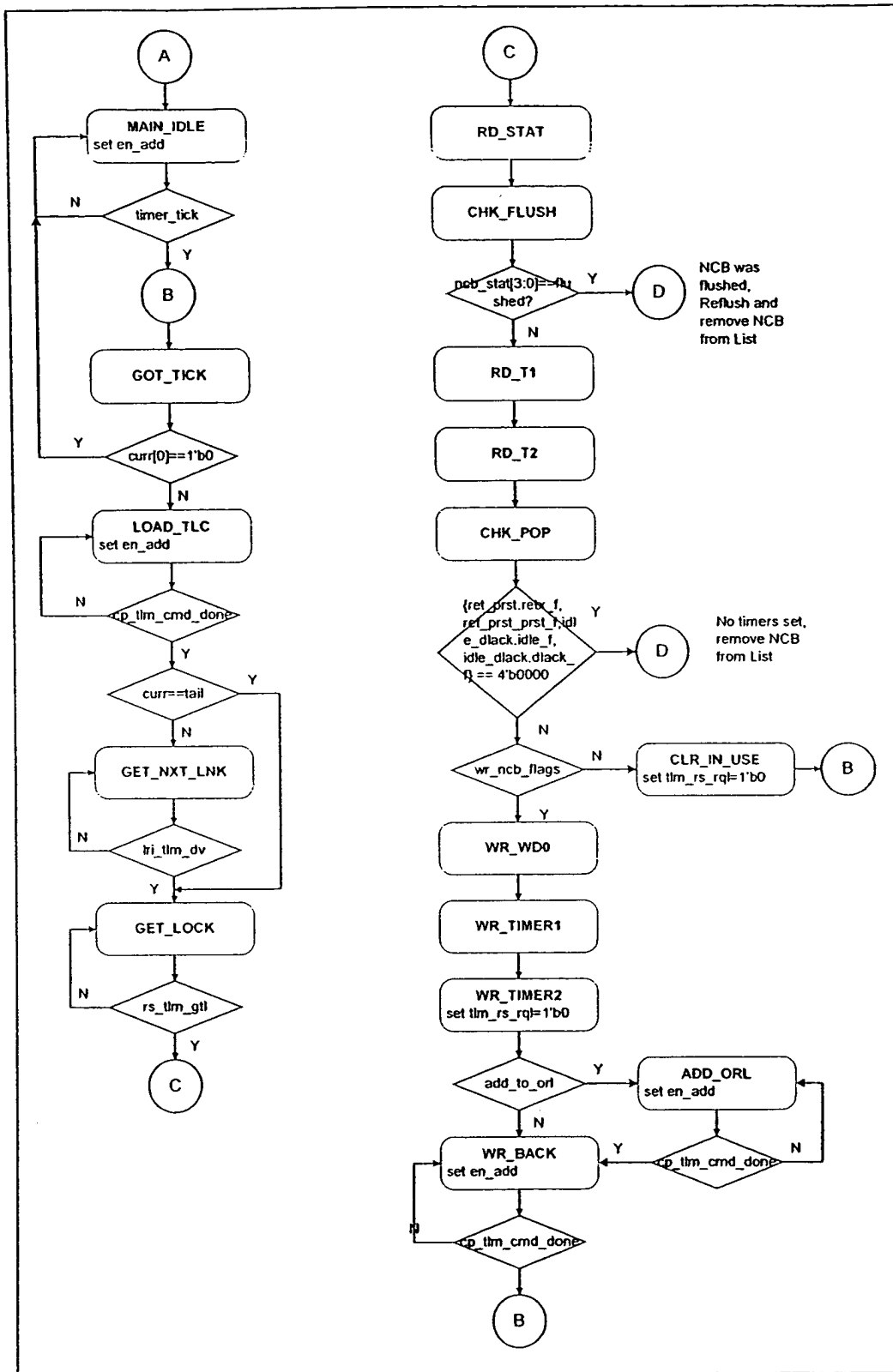


Figure 3D1 Timer List Manager State Machine

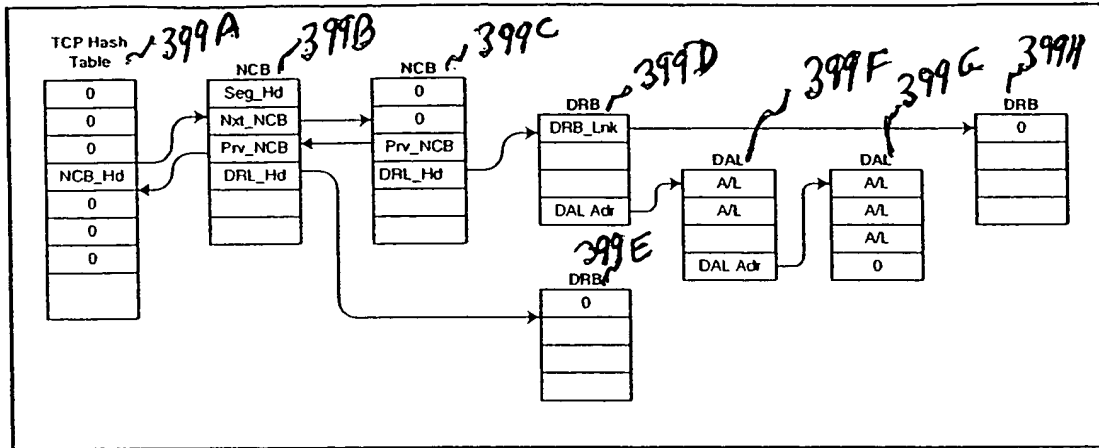


Figure 3E

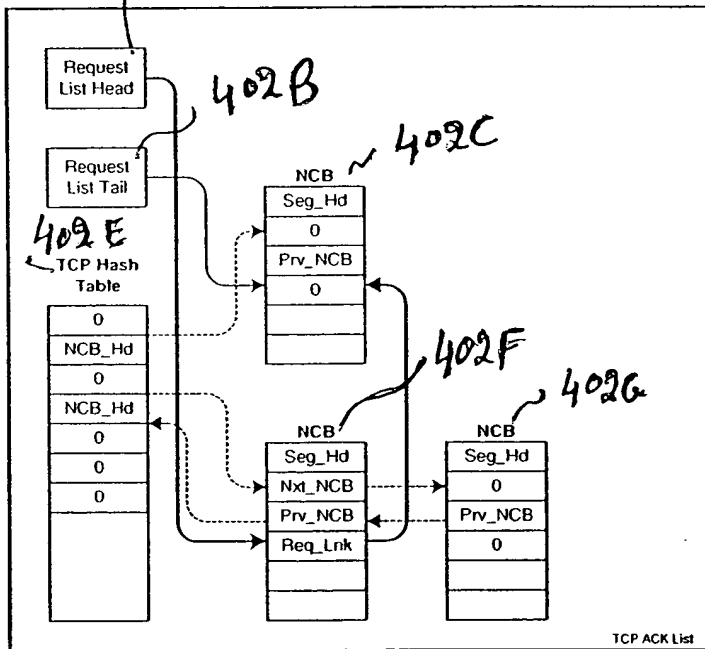


Figure 3F

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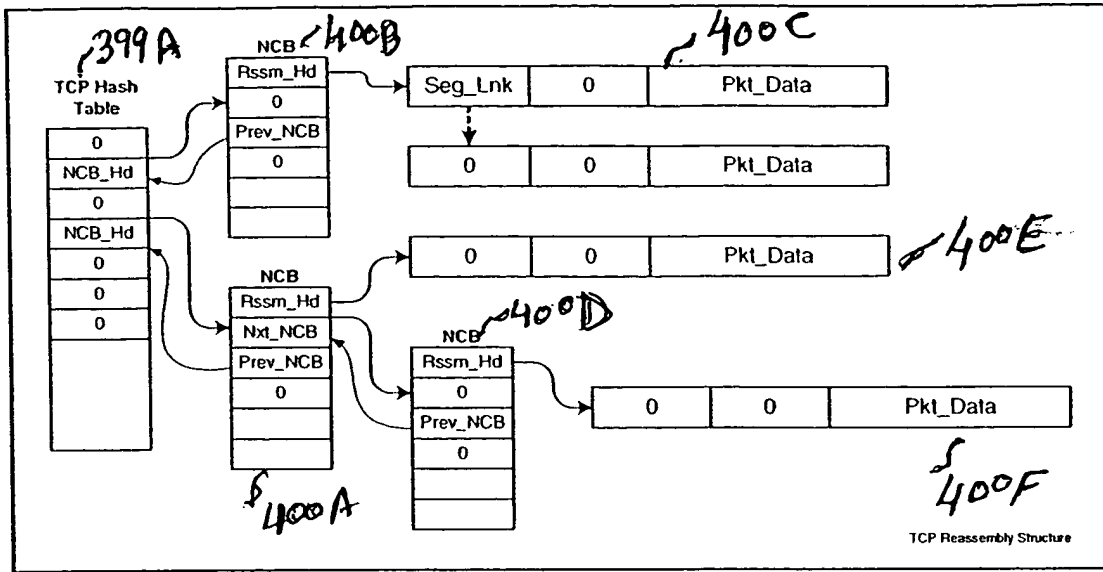


Figure 3G

400

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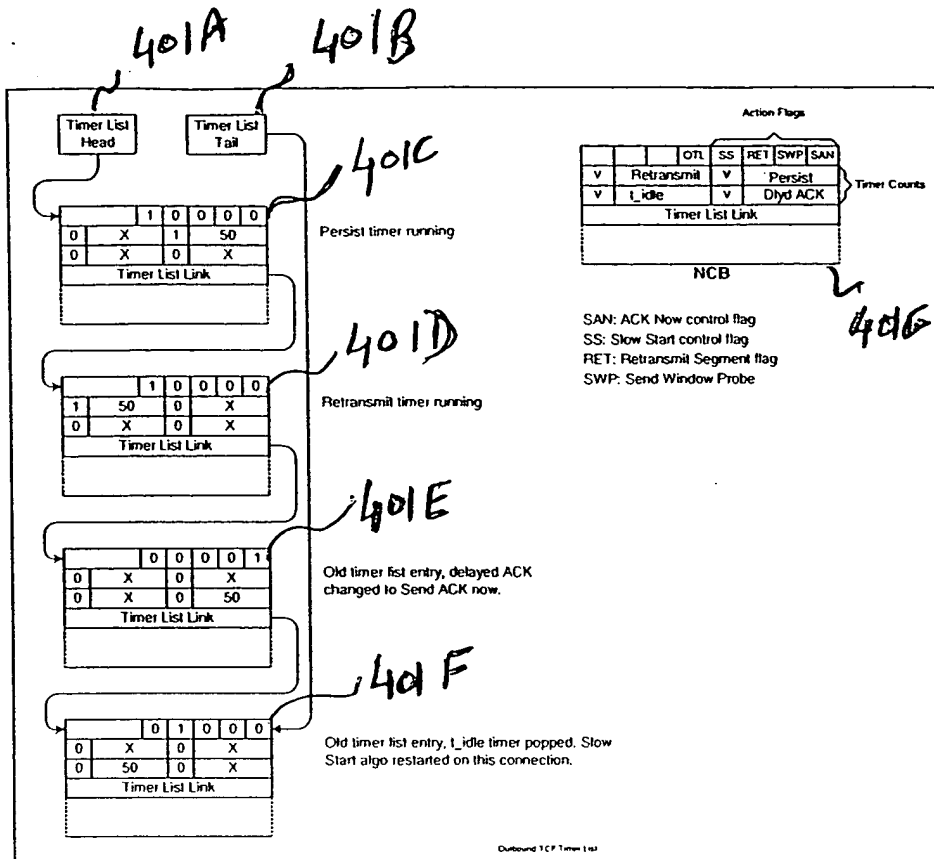


Figure 3H

401

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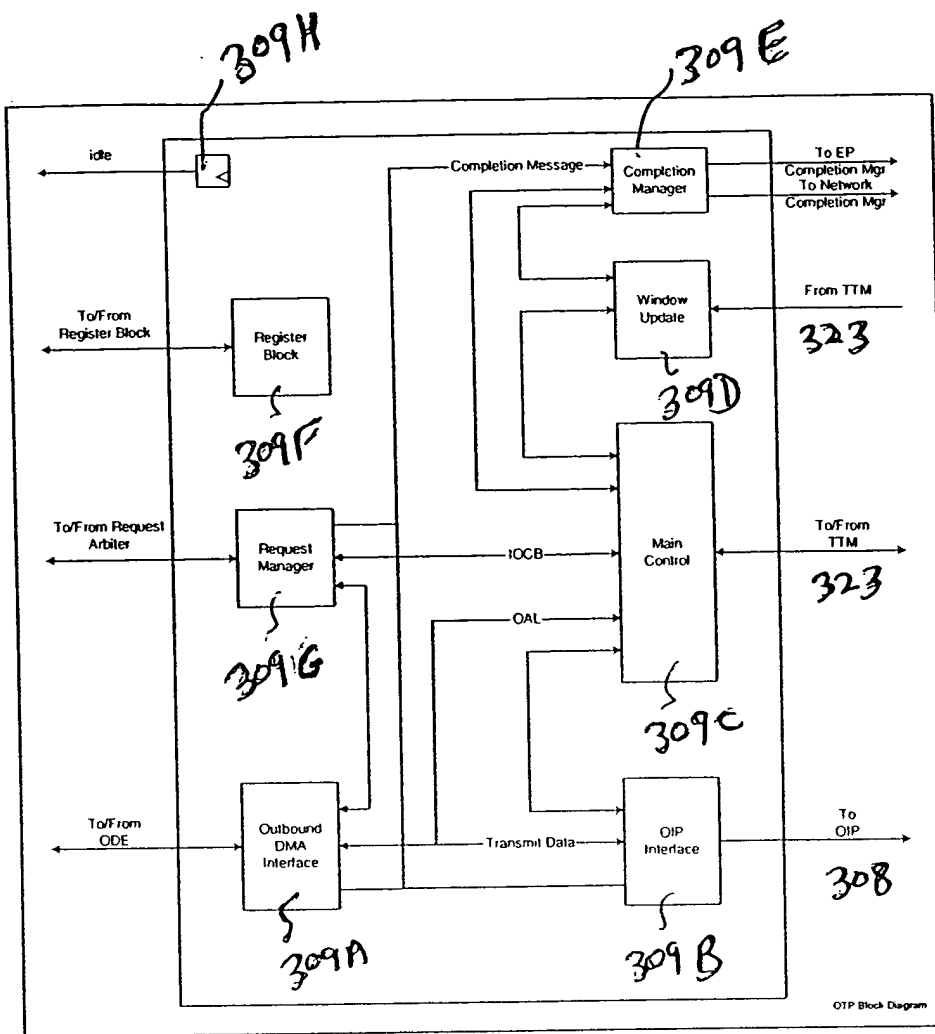


Figure 31

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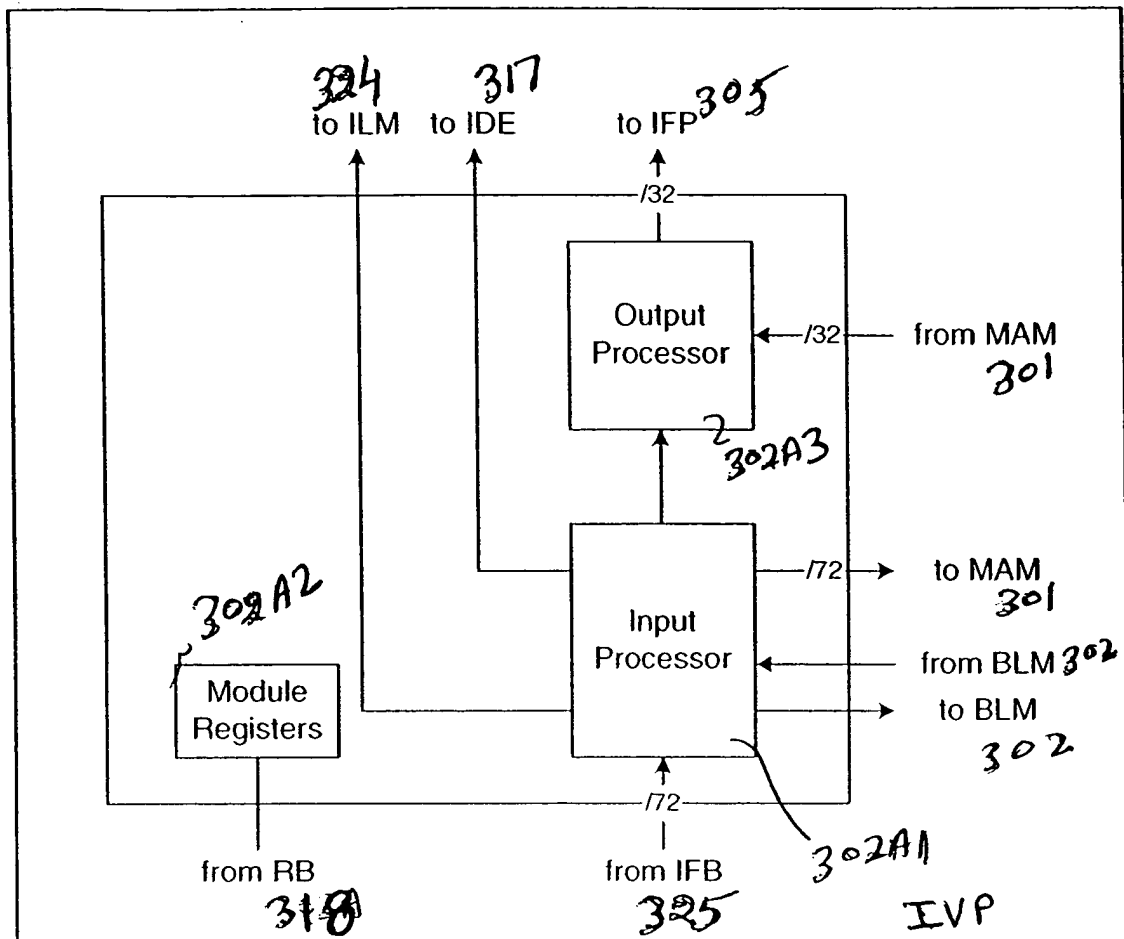


Figure 3J1

302A

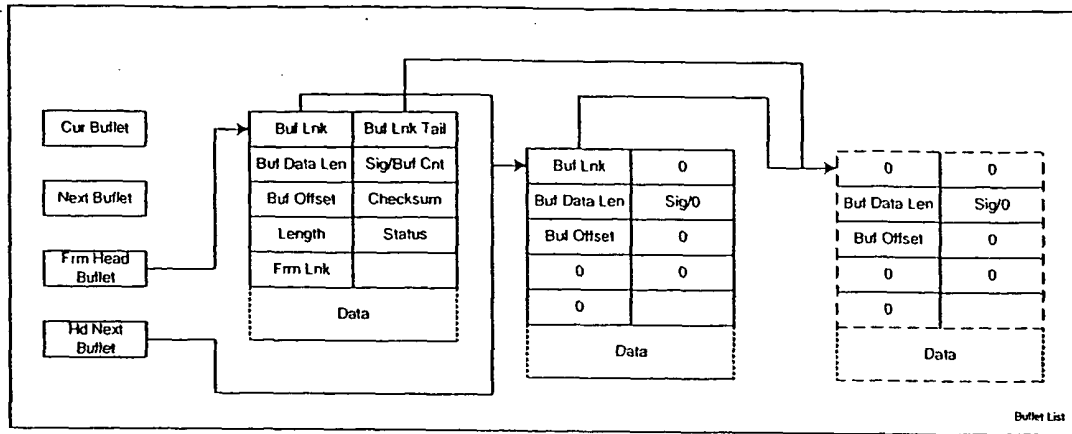


Figure 3J1 Buffer List Structure

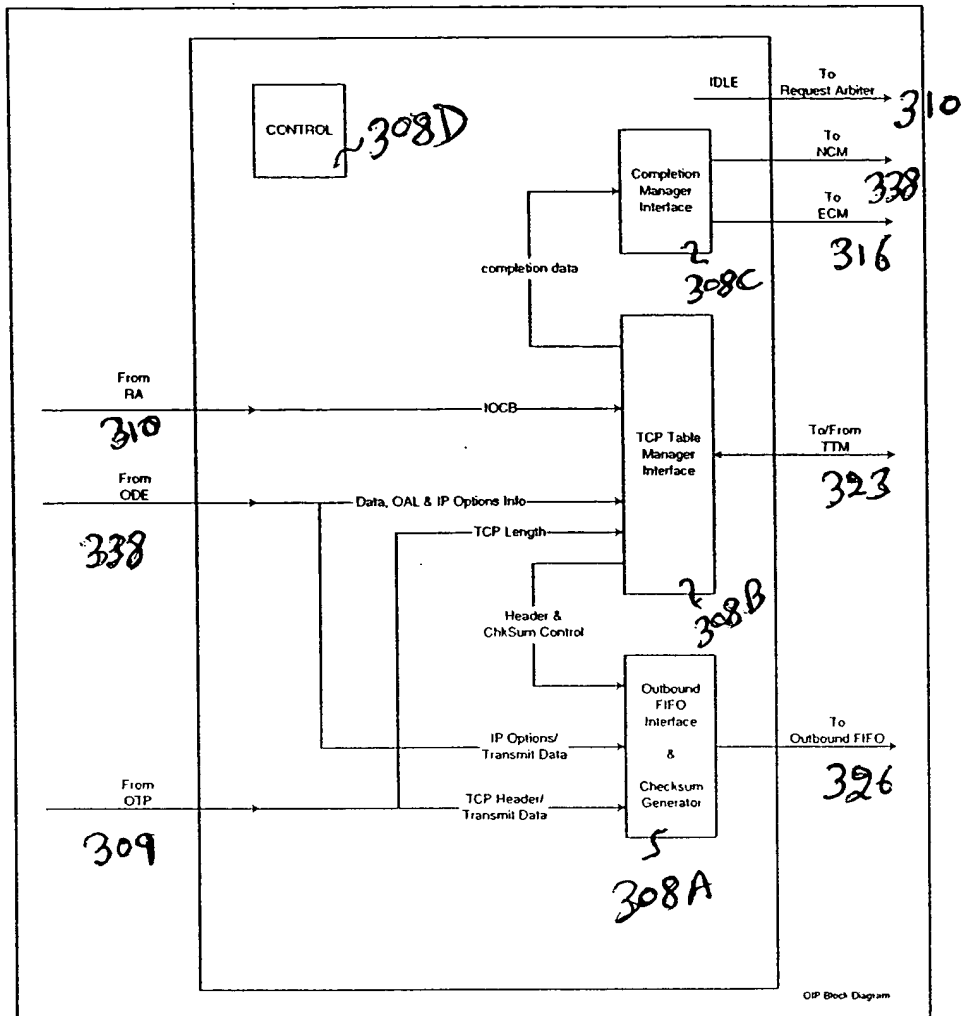


Figure 3K

308

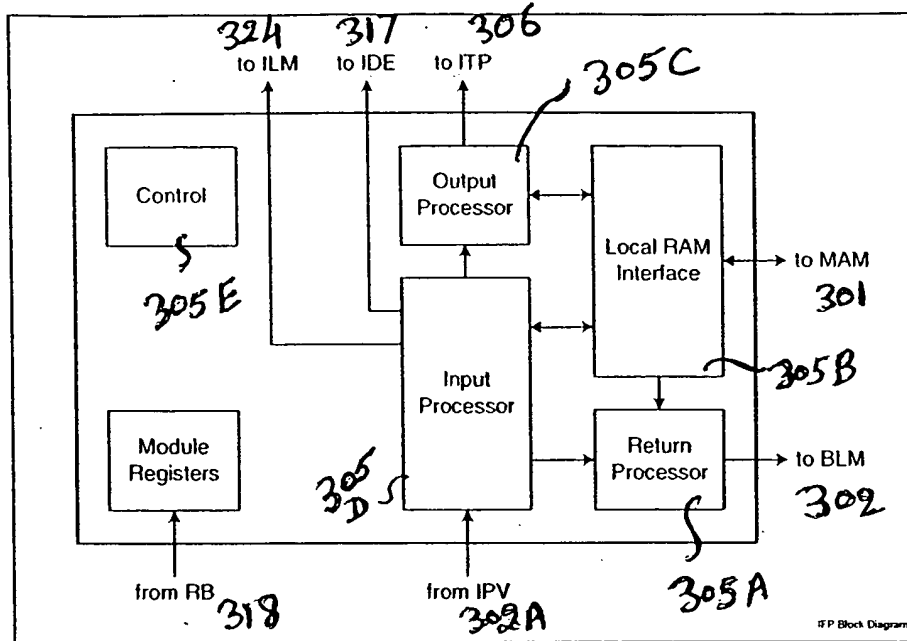


Figure 3L

305

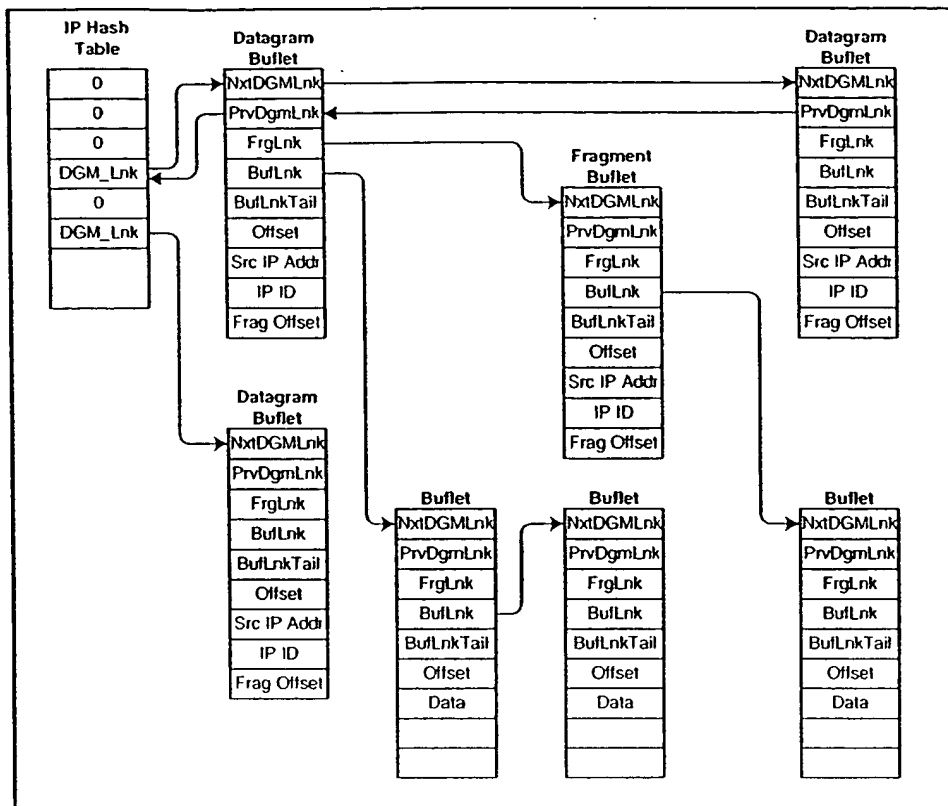


Figure 3L1:

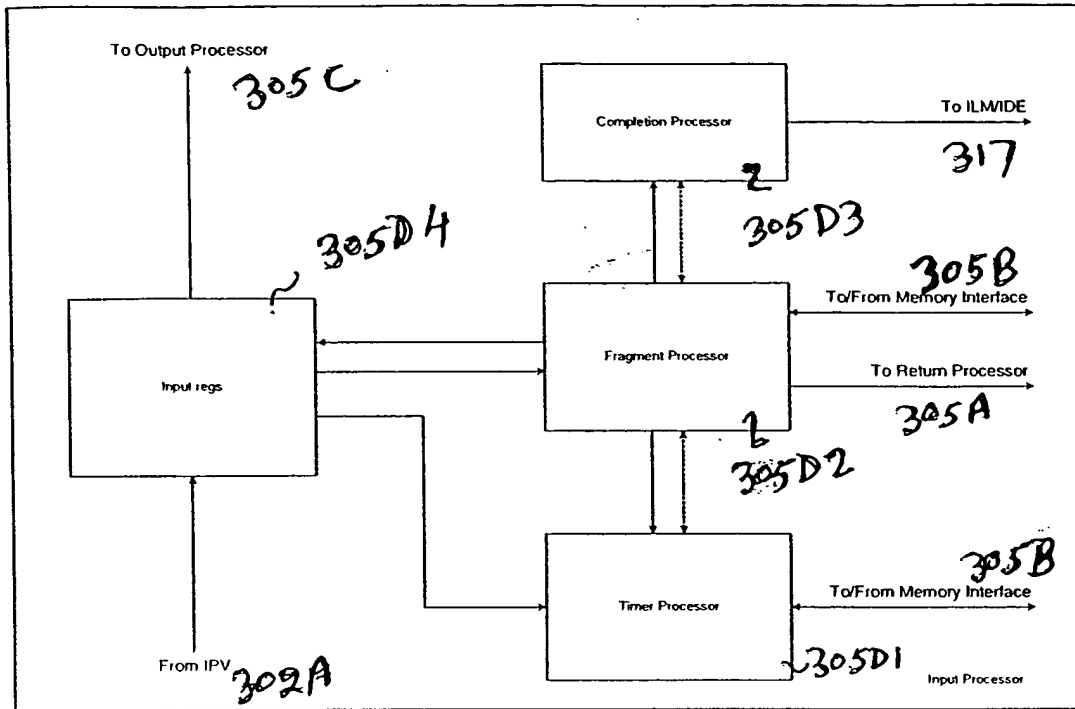


Figure 3L2

305D

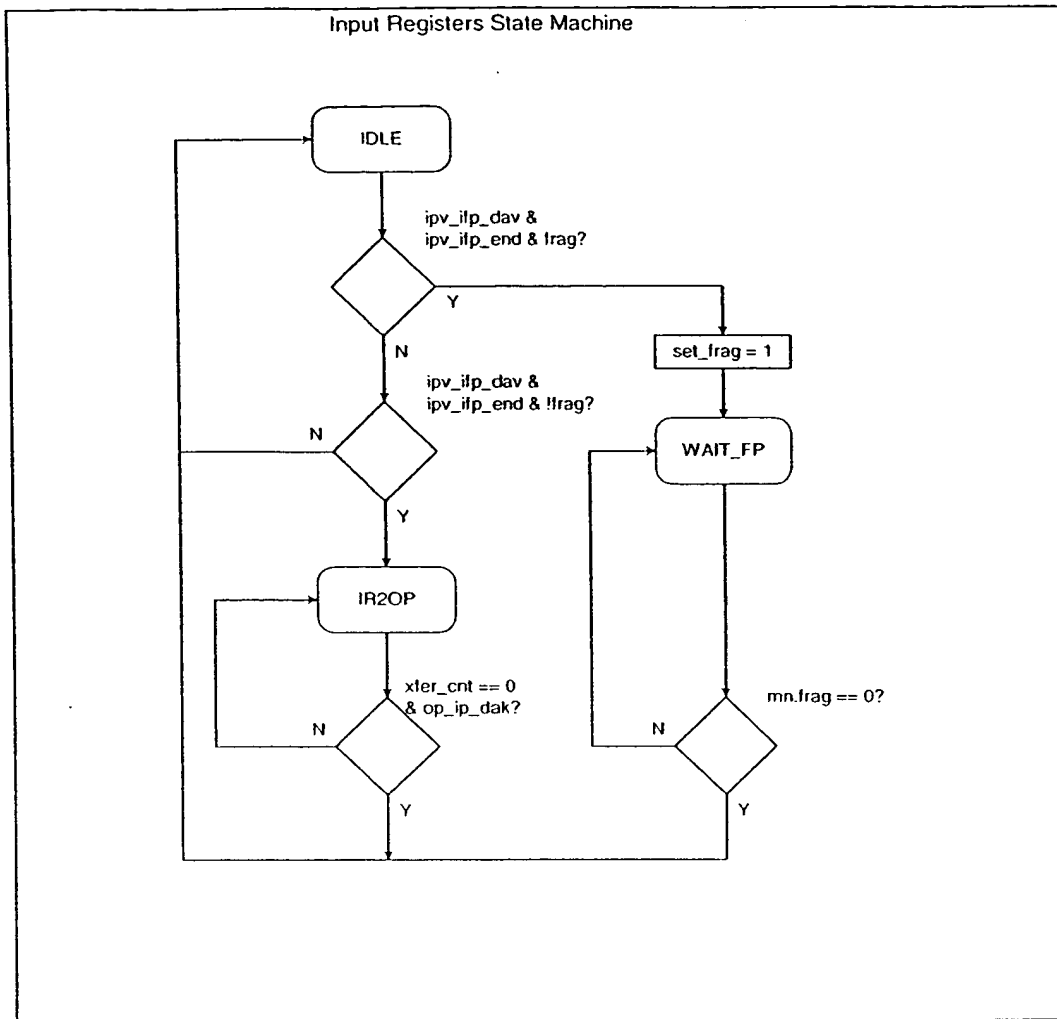


Figure 3L3

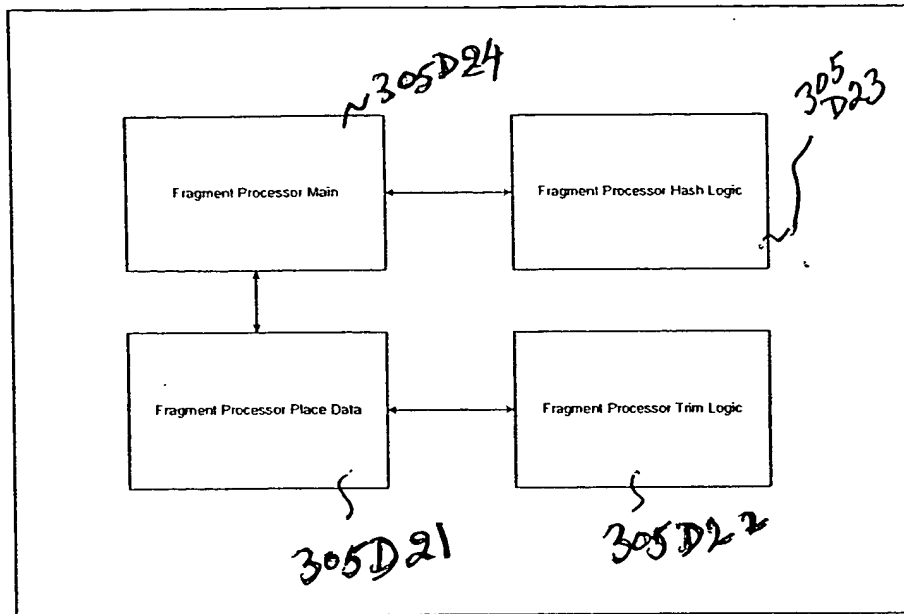


Figure 3L4

305D2

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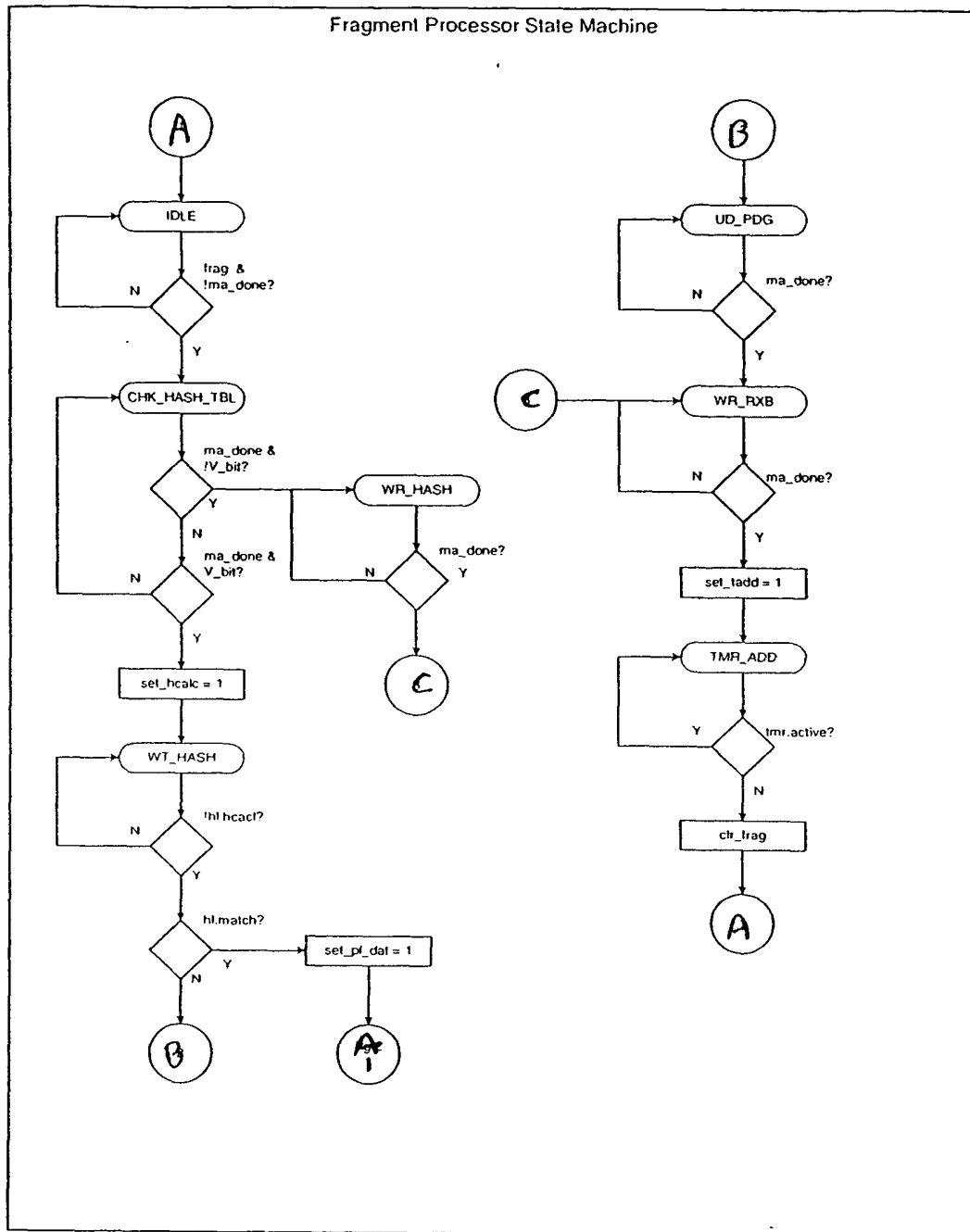


Figure 3L5A I

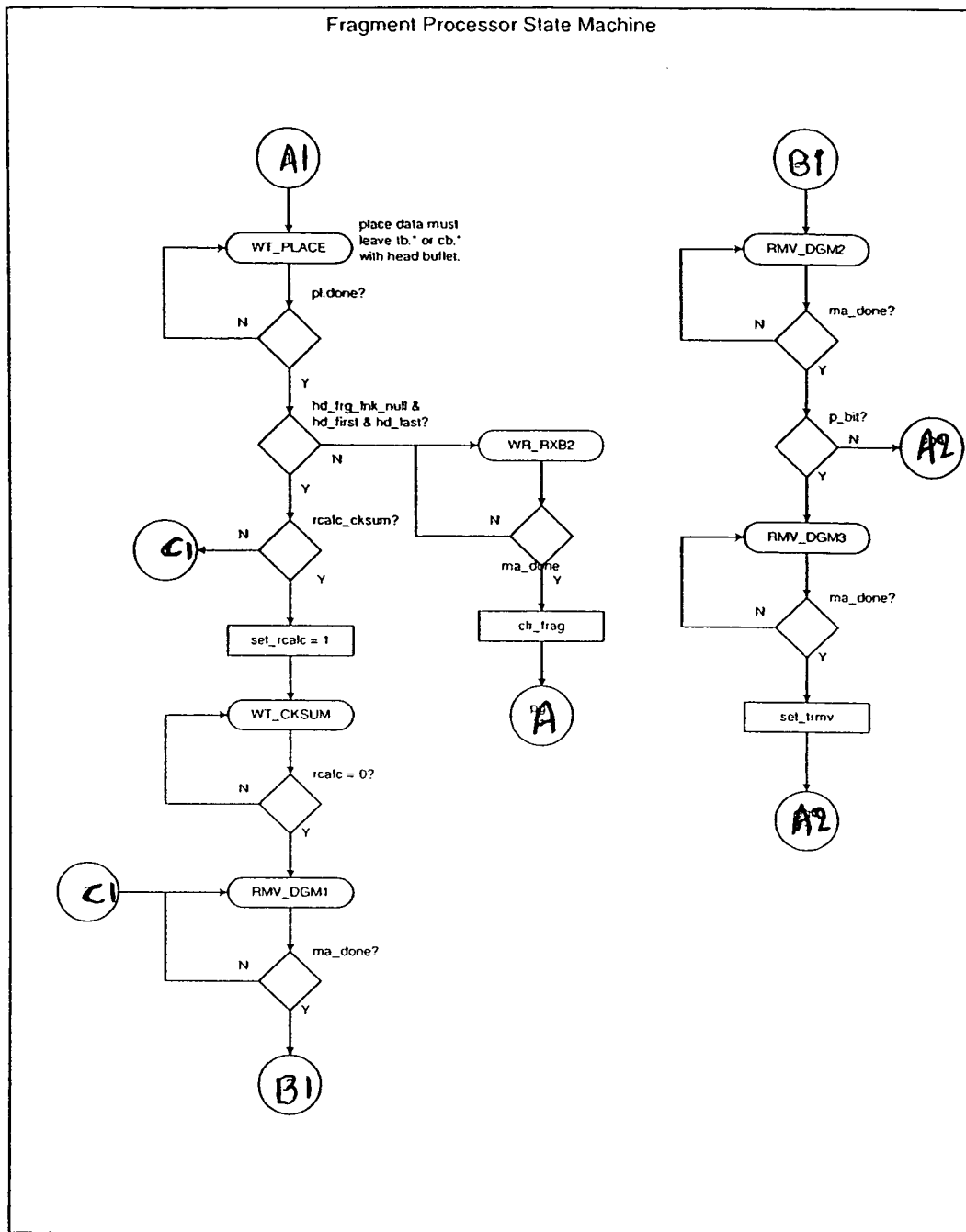


Figure 3L5B F

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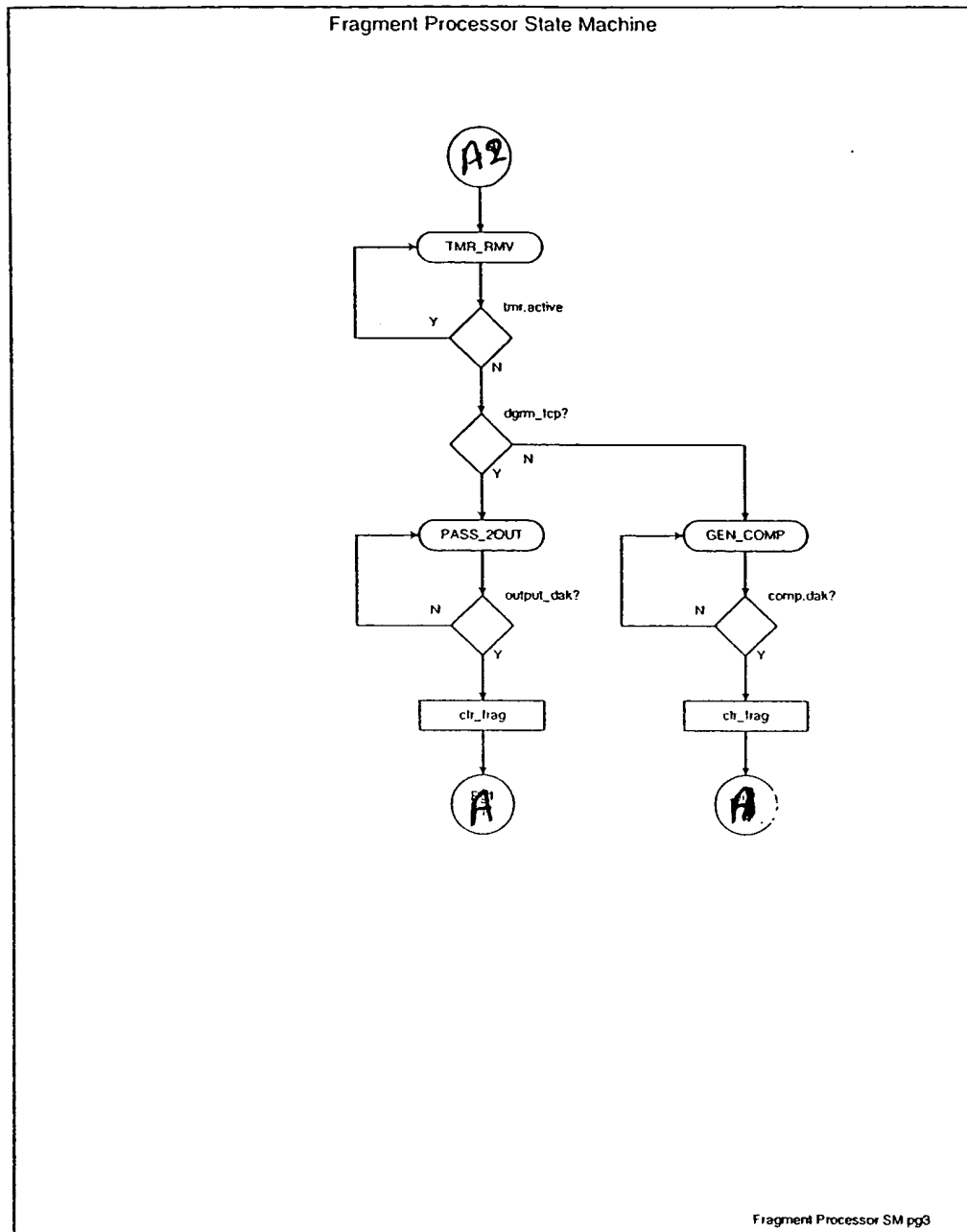


Figure 3L5C

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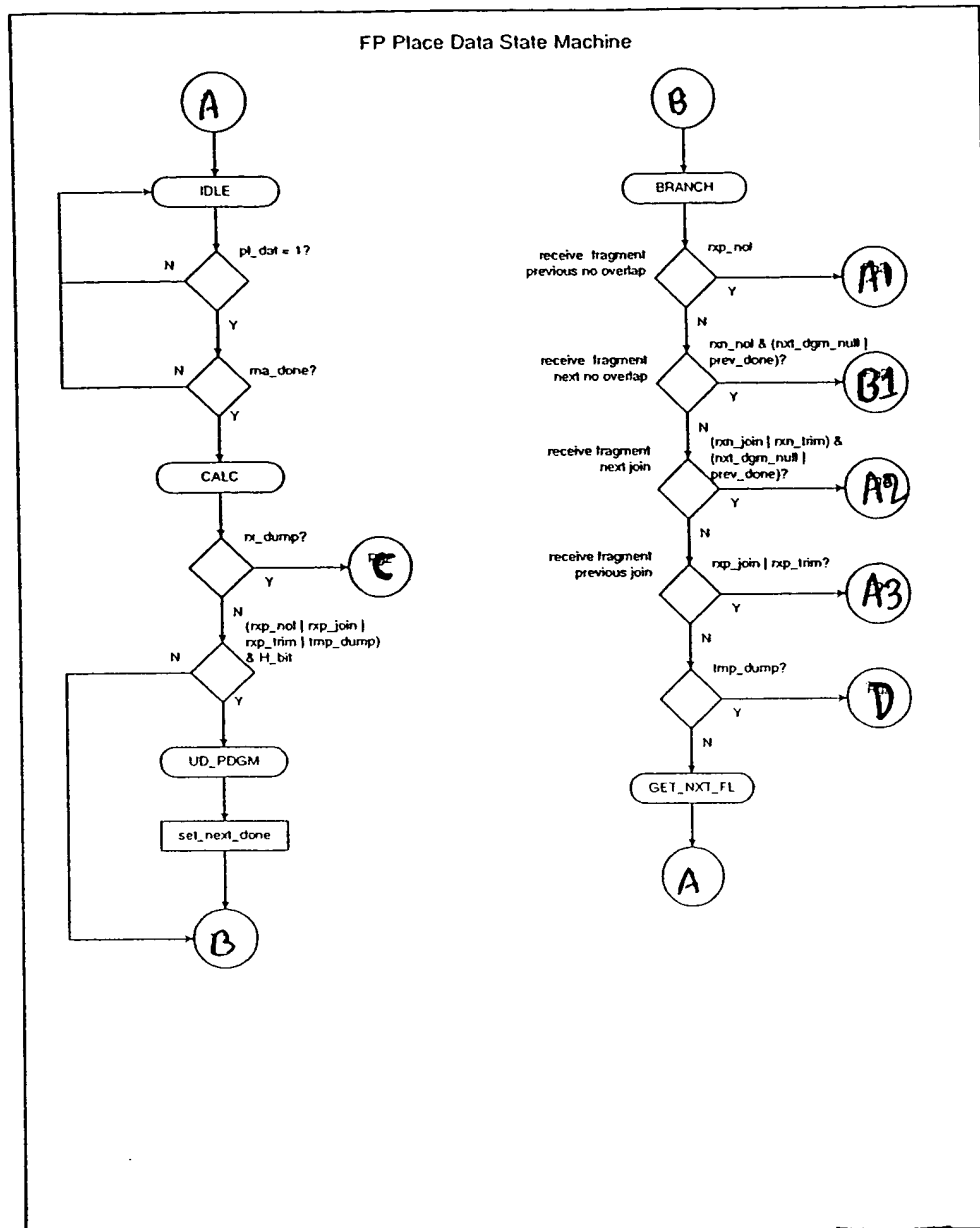


Figure 3L6A

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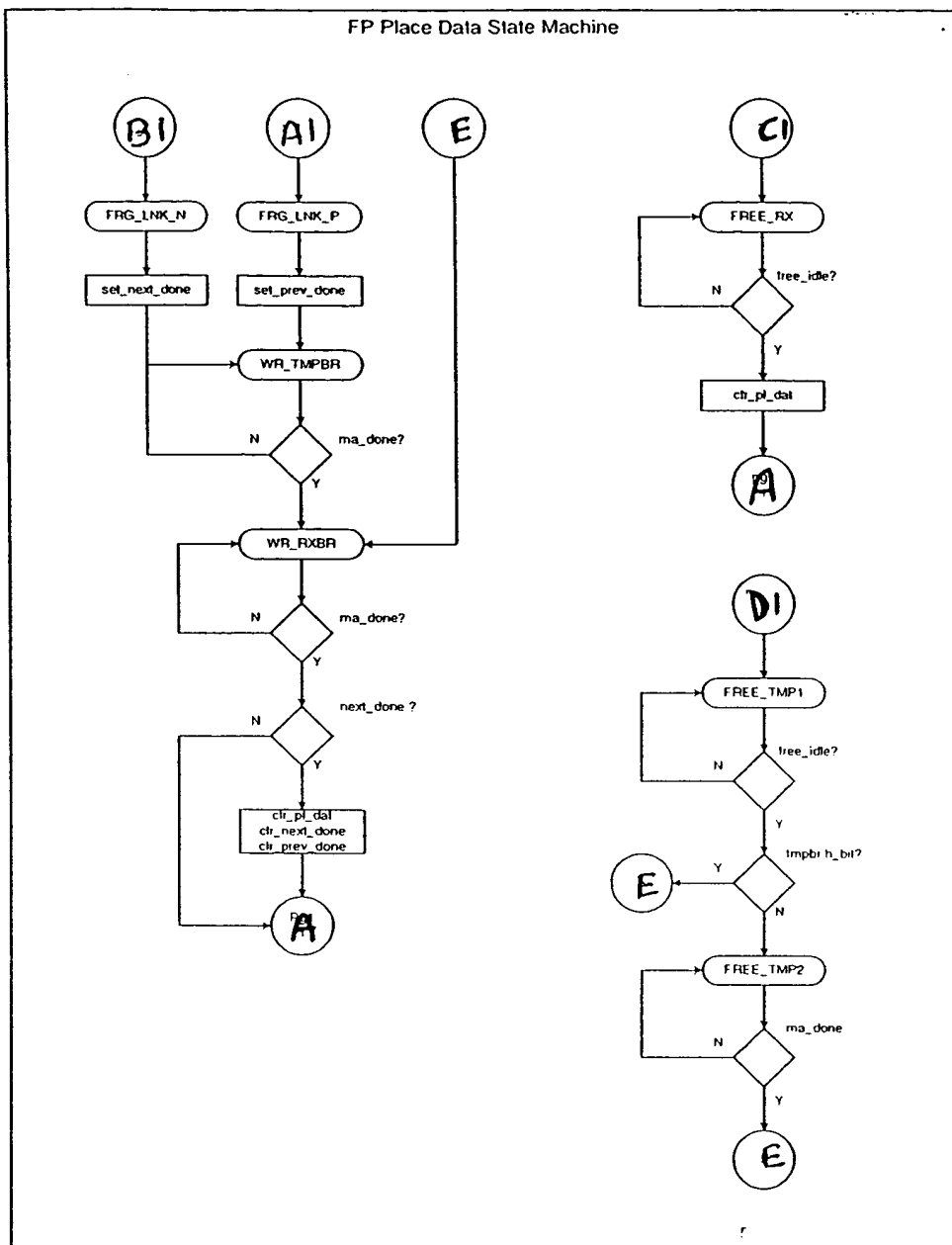


Figure 3L6 B

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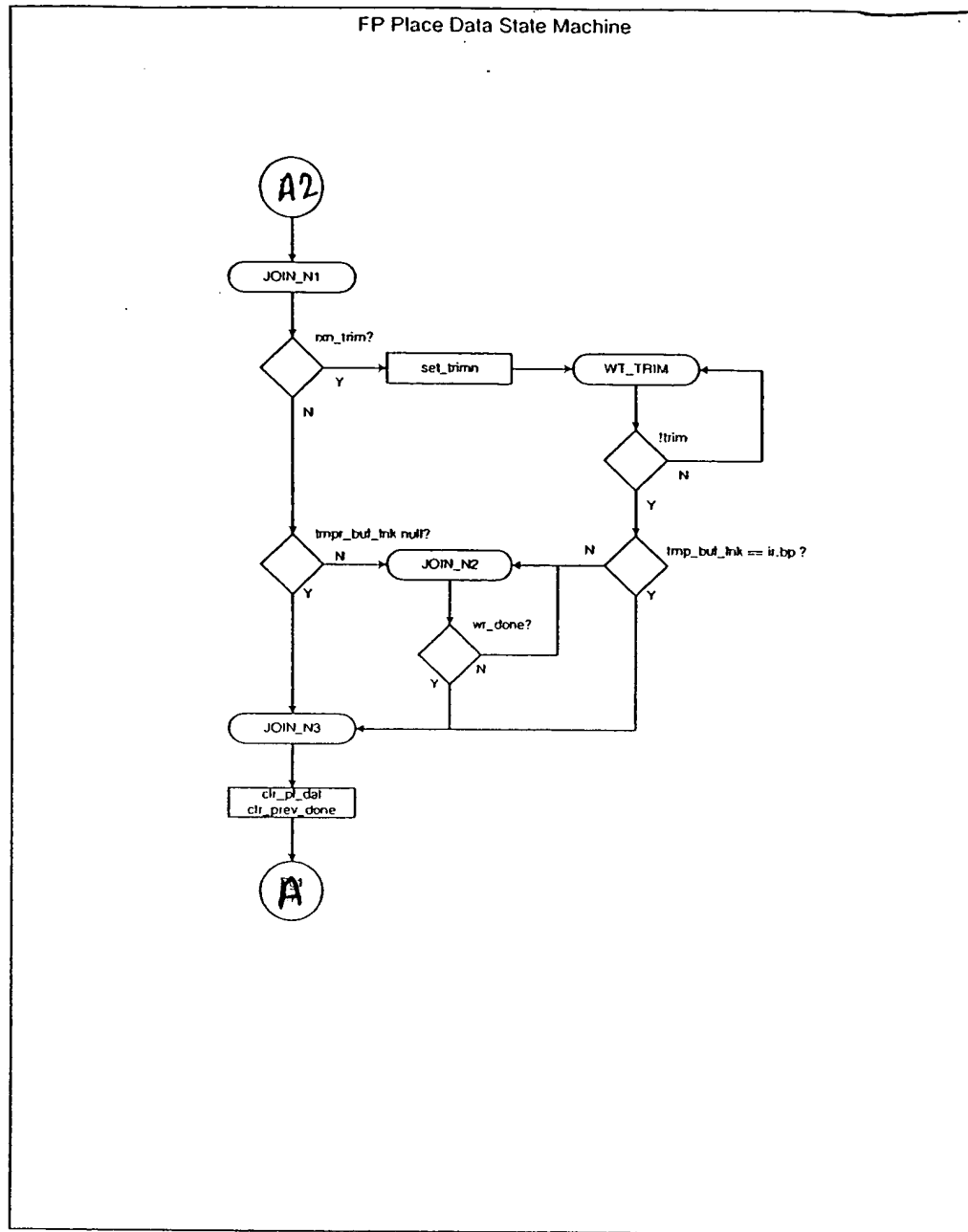


Figure 3L6C

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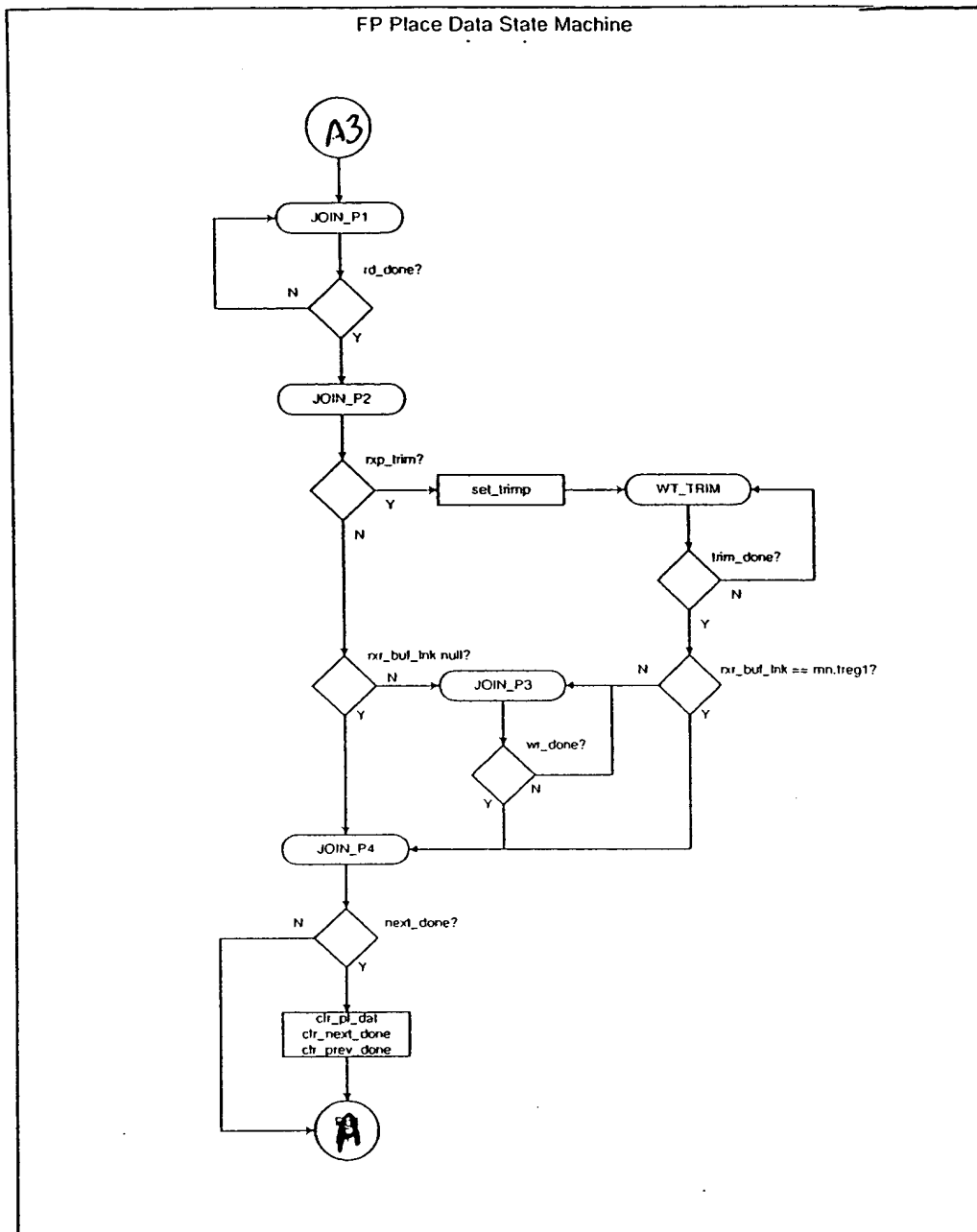


Figure 3L6D

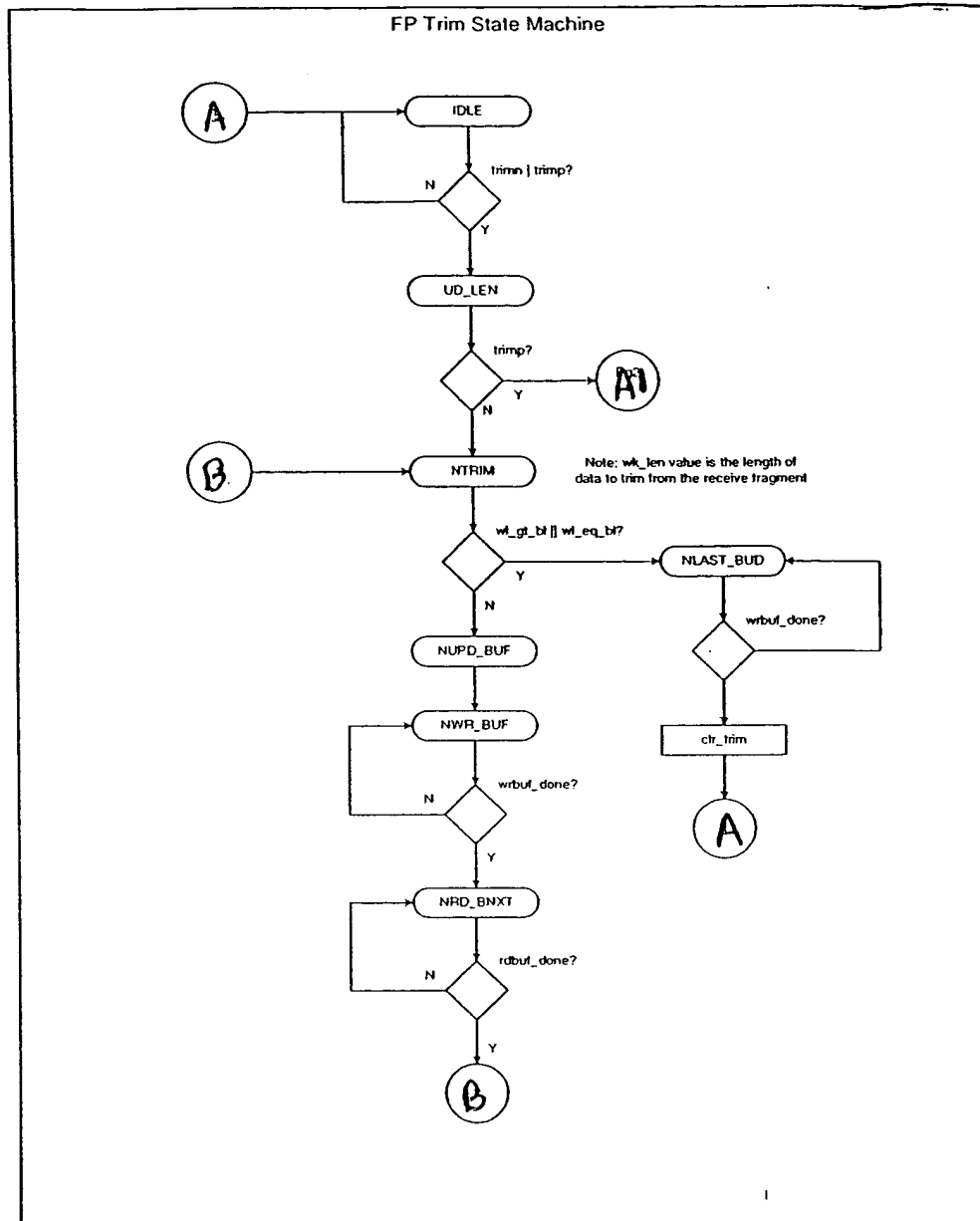


Figure 3L7A

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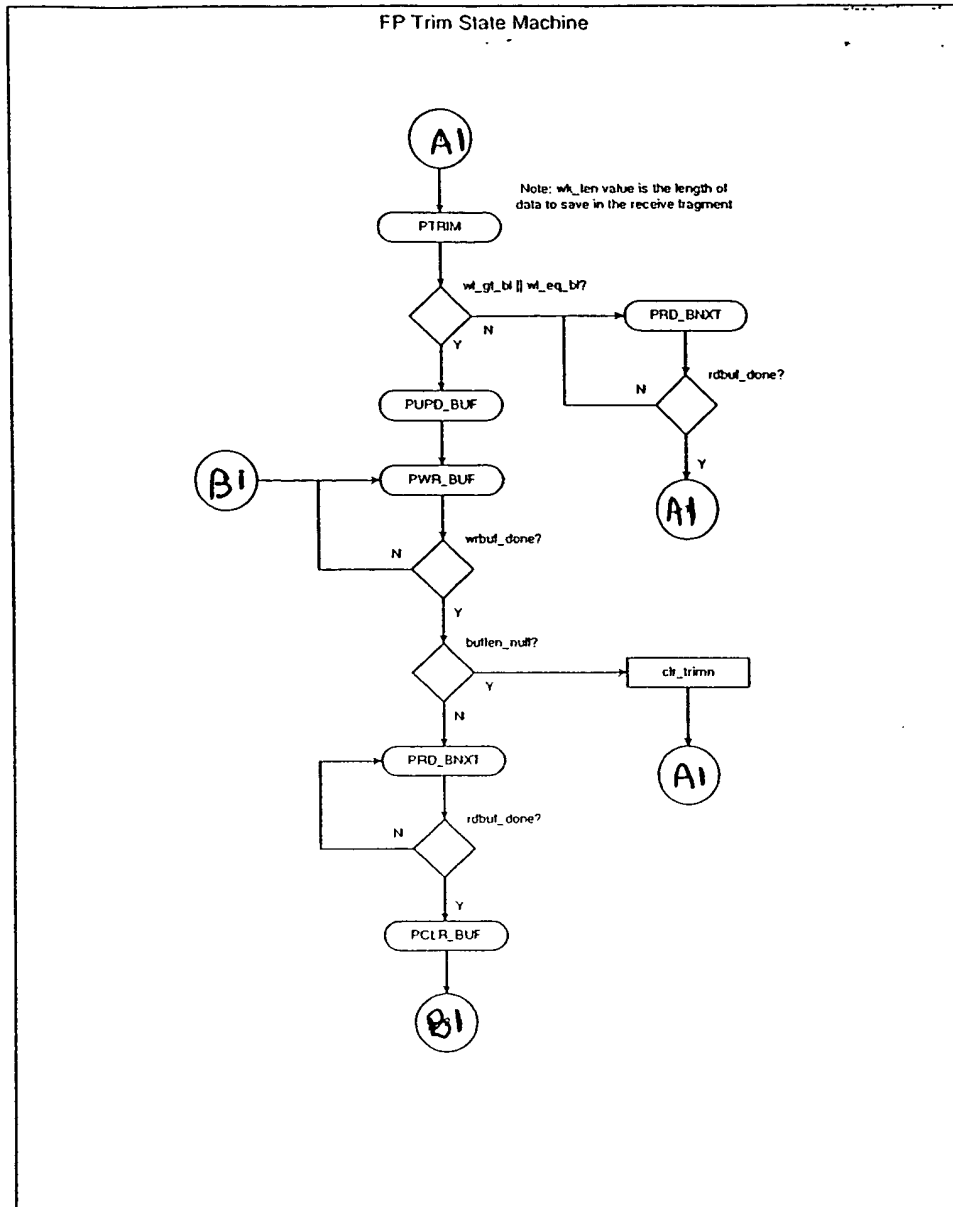


Figure 3L7B:

46 0651

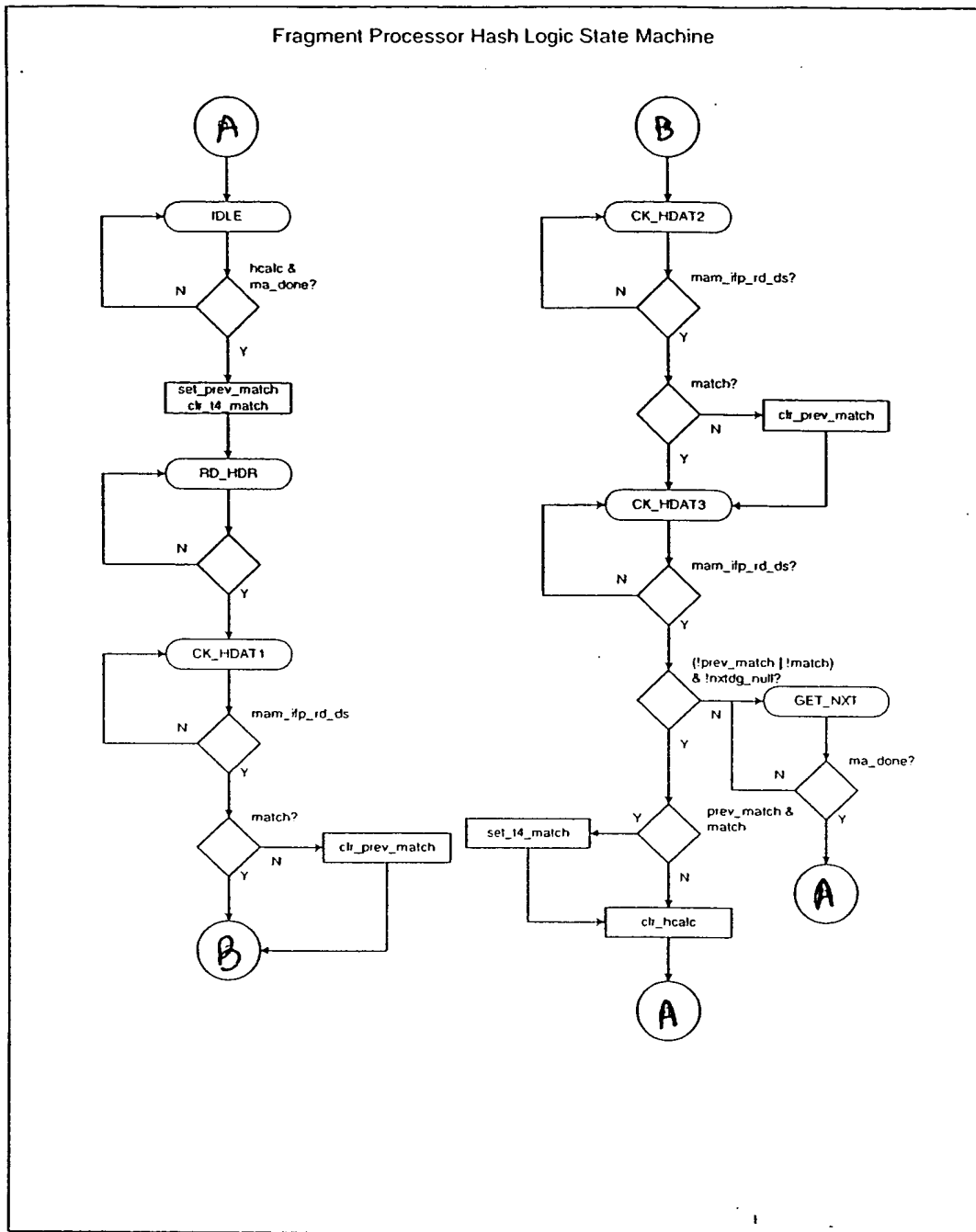


Figure 3L8 I

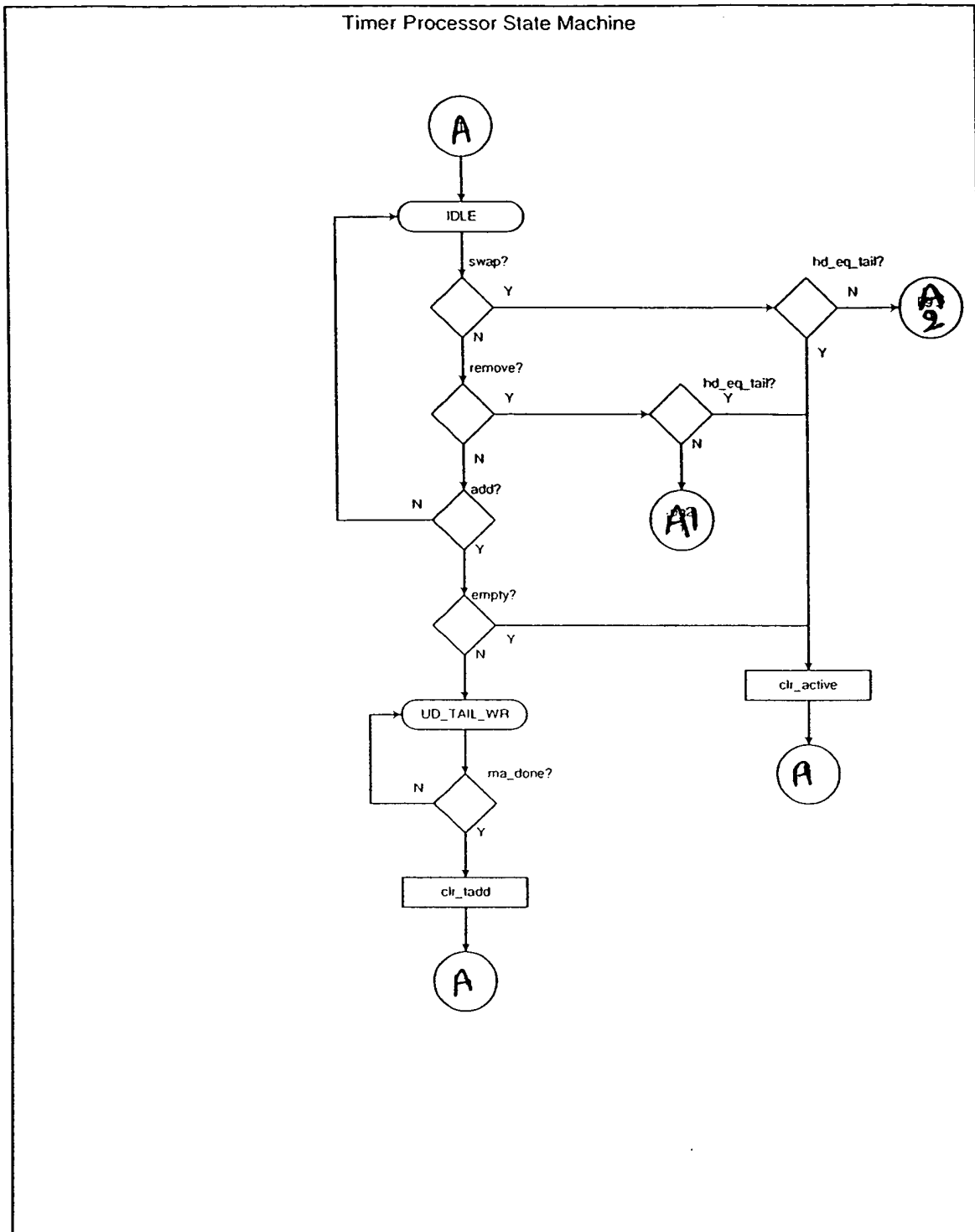


Figure 3L9A

48051

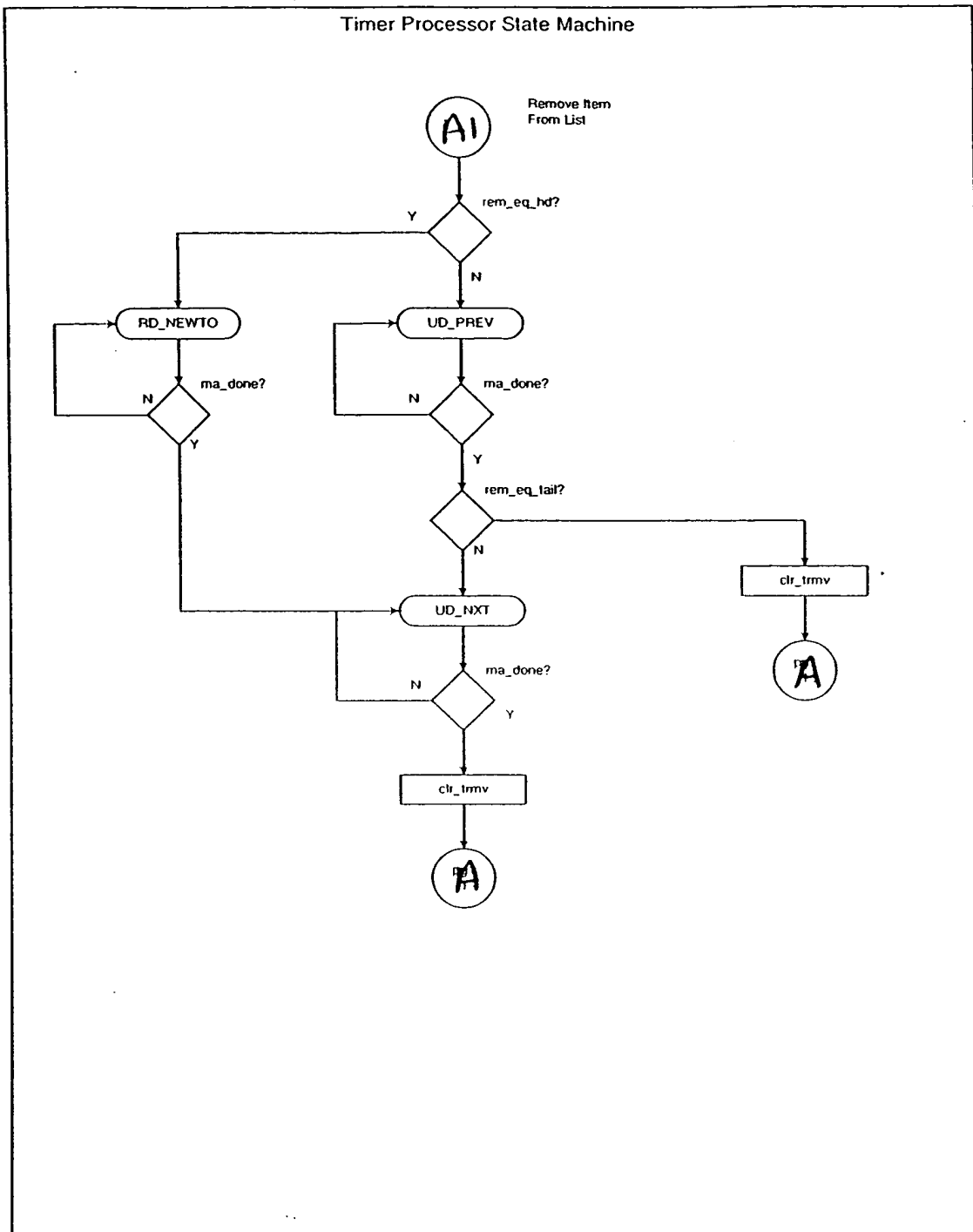


Figure 3L9B:

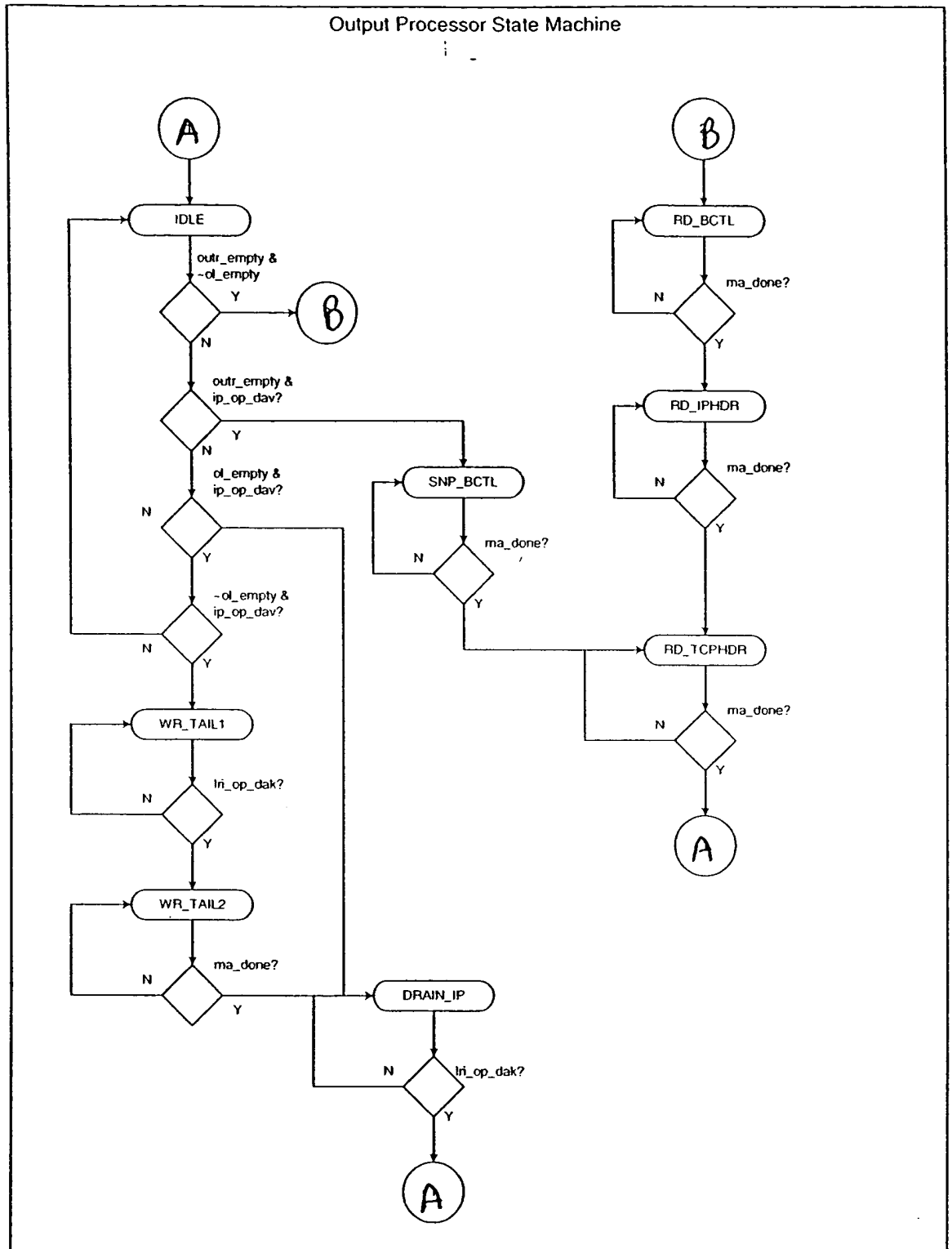


Figure 3L10 -

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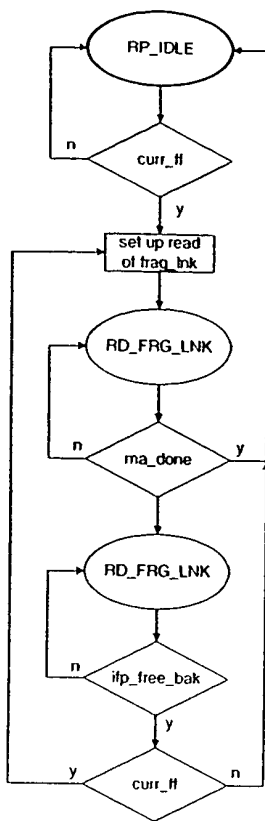


Figure 3L11 - Return Processor State Machine

404

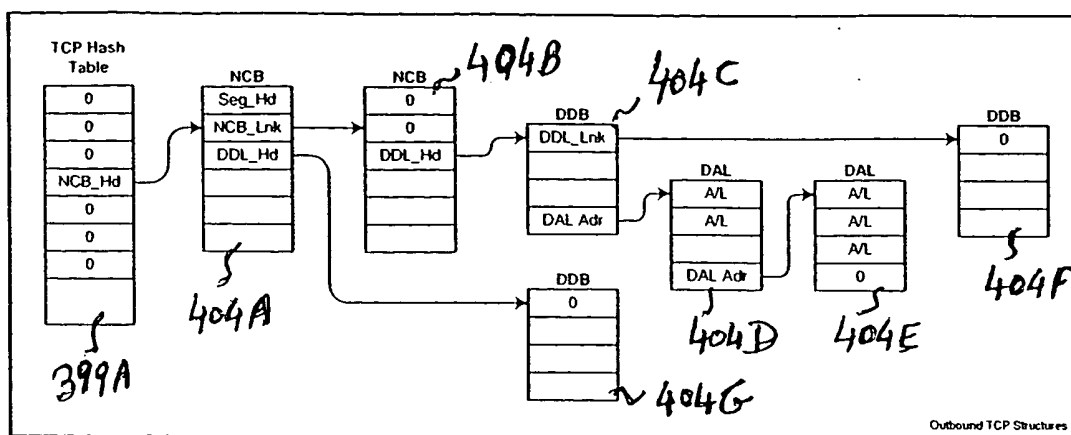


Figure 4A

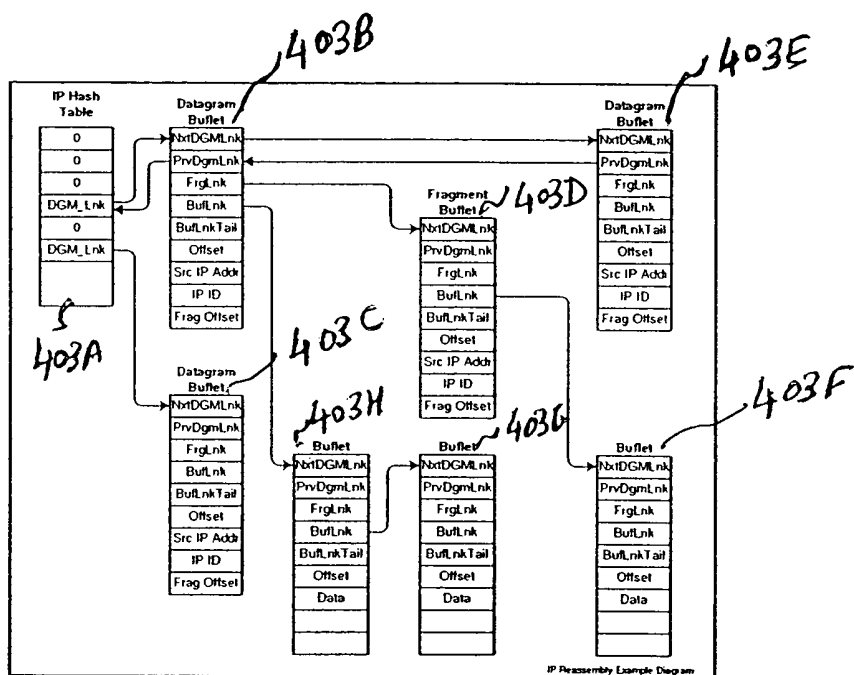


Figure 4B

403